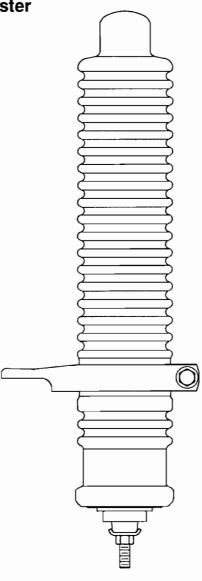


Surge Arresters

VariSTAR® Type AZL Heavy Duty Distribution Class Surge Arrester Certified Design Test Report



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SECTION 1 - GENERAL INFORMATION

1.1 Purpose and Objectives Statement

The purpose of this document is to present certified design test data and a summary of physical and electrical characteristics of the McGraw-Edison VariSTAR® Type AZL Heavy Duty Distribution Class Surge Arrester.

1.2 Certification Statement

The design tests conducted and the data recorded in this document are presented in accordance with the sections of ANSI/IEEE Standard C62.11-1987 relevant to Heavy Duty Distribution Class Metal Oxide Surge Arresters.

The McGraw-Edison VariSTAR® Type AZL Heavy Duty Distribution Class surge arresters rated 3 kV to 36 kV meet or exceed all applicable requirements of the above referenced standard as reported in the following sections of this document.

1.3 Summary Data

Tabulated below are summaries of the electrical and physical characteristics of the McGraw-Edison VariSTAR® Type AZL Heavy Duty Distribution Class surge arresters rated 3 kV to 36 kV

TABLE 1
Protective Characteristics

Arrester Rating	MCOV	Protective Level*		Switching Surge**					
kV rms	kV rms		1.5 kA	3 kA	5 kA	10 kA	20 kA	40 kA	kV crest
3	2.55	10.7	8.2	8.7	9.2	10.0	11.3	13.6	7.4
6	5.10	21.4	16.4	17.4	18.4	20.0	22.5	27.1	14.7
9	7.65	32.1	24.5	26.1	27.5	30.0	33.8	40.7	22.1
10	8.40	35.3	27.0	28.7	30.3	33.0	37.2	44.7	24.3
12	10.2	42.8	32.7	34.7	36.7	40.0	45.0	54.2	29.4
15	12.7	53.5	40.9	43.4	45.9	50.0	56.3	67.8	36.8
18	15.3	64.2	49.1	52.1	55.1	60.0	67.6	81.4	44.1
21	17.0	74.9	57.3	60.8	64.3	70.0	78.8	94.9	51.5
24	19.5	84.3	64.4	68.4	72.3	78.8	88.7	106.8	57.9
27	22.0	95.2	72.8	77.3	81.7	89.0	100.2	120.7	65.5
30	24.4	105.9	81.0	86.0	90.9	99.0	111.5	134.2	72.8
36	30.4	124.8	95.4	101.3	107.0	116.6	131.3	158.1	85.8

^{*}Based on a 10 kA current impulse that results in a discharge voltage cresting in 0.5 µs

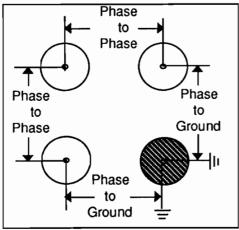


Figure 1
Centerline Diagram for Table 2

TABLE 2 Mounting Distances and Clearances for Type AZL Arresters

NOTE: These are recommended minimum clearances only. As such they are not intended to take precedence over existing construction codes and specifications.

Arrester Rating	Mounting Dista	ances*(in.)
kV rms	Phase to Phase	Phase to Ground
3	4.5	2.8
6	5.8	3.8
9	7.2	5.0
10	7.2	5.0
12	7.8	5.8
15	8.0	6.5
18	11.0	9.0
21	11.0	9.0
24	12.8	10.5
27	12.8	10.5
30	12.8	10.5
36	13.9	11.5

^{*}All distances are figured from the arrester centerline

^{**45-60} µs rise time 500 A surge

Section 1 (con't)

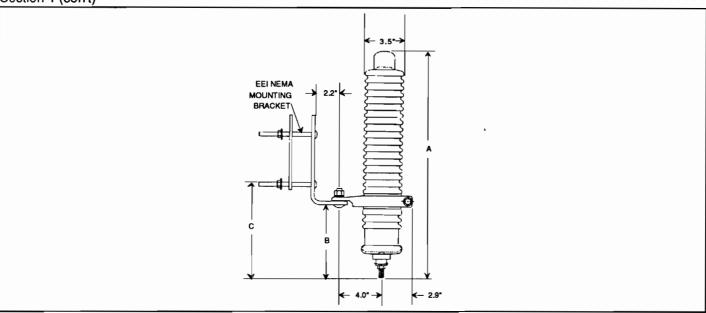


Figure 2
Arrester Diagram for Table 3 and Table 4

TABLE 3
Physical Characteristics

Arrester			Dimen	sions				
Rating		4		В	•	C		
kV rms	in.	cm.	in.	cm.	in.	cm.		
3	8.6	21.7	3.6	9.1	6.2	15.6		
6	11.0	29.0	4.6	11.6	7.1	18.1		
9	14.4	36.7	5.0	12.7	7.6	19.2		
10	14.4	36.7	5.0	12.7	7.6	19.2		
12	17.1	43.3	5.8	14.8	8.4	21.3		
15	17.1	43.3	5.8	14.8	8.4	21.3		
18	20.6	52.2	6.0	15.3	8.6	21.8		
21	25.6	65.0	8.8	22.3	11.3	28.8		
24	25.6	65.0	8.8	22.3	11.3	28.8		
27	25.6	65.0	8.8	22.3	11.3	28.8		
30	28.6	72.7	8.8	22.4	11.4	28.9		
36	28.6	72.7	8.8	22.4	11.4	28.9		

TABLE 4
Insulation Characteristics

Arrester	Creepage	Distance	Strike Di	stance	Insulation	Withstand Vo	ltages
Rating					1.2/50 µs impulse	1 min Dry	10 sec Wet
kV rms	in.	cm.	in.	cm.	kV crest	kV rms	kV rms
3	2.2	5.6	1.8	4.6	45	20	15
6	5.4	13.7	4.5	11.4	85	50	40
9	9.0	22.9	6.8	17.3	115	70	55
10	9.0	22.9	6.8	17.3	115	70	55
12	11.4	29.0	8.8	22.4	135	85	65
15	11.4	29.0	8.8	22.4	135	85	65
18	15.5	39.4	11.8	30.0	165	110	80
21	18.9	48.0	14.2	36.2	190	130	95
24	18.9	48.0	14.2	36.2	190	130	95
27	18.9	48.0	14.2	36.2	190	130	95
30	23.0	58.4	17.5	44.5	210	150	125
36	23.0	58.4	17.5	44.5	210	150	125

SECTION 2 - DESIGN TEST DATA

2.0 Definition of Terms

Surge Arrester

 A protective device for limiting surge voltages on equipment by diverting surge current and returning the device to its original status. It is capable of repeating these functions as specified.

Metal-Oxide Surge Arrester (MOSA)

- A surge arrester utilizing valve elements fabricated from nonlinear resistance metal oxide materials.

Duty-cycle Voltage Rating

-The designated maximum permissible voltage between terminals at which the arrester is designed to perform its duty cycle.

Maximum Continuous

-The maximum designated root-mean-square (rms) value of power frequency voltage Operating Voltage (MCOV) that may be applied continuously between the terminals of the arrester.

Prorated Section

 A complete, suitably housed part of an arrester, comprising all necessary components. including gaseous medium, in such a proportion as to accurately represent, for a particular test, the characteristics of a complete arrester.

Arrester Disconnector

- A means for disconnecting an arrester in anticipation of, or after a failure in order to prevent a permanent fault on the circuit and to give indication of a failed arrester.

Design Tests

- Tests made on each design to establish the performance characteristics and to demonstrate compliance with the appropriate standards of the industry. Once made they need not be repeated unless the design is changed so as to modify performance.

Certification Tests

 Tests run on a regular periodic basis to verify that selected, key performance characteristics of a product or representative samples thereof have remained within performance specifications.

Routine Tests

- Tests made by the manufacturer on every device or representative samples, or on parts or materials, as required, to verify that the product meets the design specifications.

Waveshape Designation

- (1) The wave shape of an impulse (other than rectangular) of a current or voltage is designated by a combination of two numbers. The first, an index of the wave front, is the virtual duration of the wave front in microseconds. The second, an index of the wave tail, is the time in microseconds from the virtual zero to the instant at which one-half of the crest value is reached on the wave tail. Examples are 1.2/50 and 8/20 µs waves.
- (2) The wave shape of a rectangular impulse of current or voltage is designated by two numbers. The first designates the minimum value of the current or voltage that is sustained for the time in microseconds designated by the second number. An example is 75 A 2000 μs wave.

Reference Current

-The reference current (Iref) of an arrester is the peak value of the resistive component of the power frequency current used to determine the reference voltage (Vref) of an arrester. The reference current is high enough to make the effects of stray capacitance on the measured voltage negligible.

Reference Voltage

 The reference voltage of an arrester is the highest peak value of the power frequency voltage, independent of polarity, divided by the square root of two, measured at the reference current of the arrester.

Temporary Overvoltage

- A temporary overvoltage is defined as an excursion of power frequency voltage above the normal line-to-ground value. Such excursions are of variable magnitude and duration.

2.1 Arrester Insulation Withstand Tests

ANSI/IEEE C62.11-1987; Section 8.1

2.1.1 INSULATION WITHSTAND TEST DESCRIPTION

The voltage withstand tests of arrester insulation demonstrate that the insulation of the arrester is above the minimum specified levels given in Table 2 of the referenced section of the ANSI/IEEE C62.11-1987 standard.

2.1.2 INSULATION WITHSTAND TEST PROCEDURE

New, clean arresters of each rating, with internal parts removed, were mounted to the NEMA bracket and subjected to positive and negative 1.2/50 µs withstand tests and 60 Hz wet and dry withstand tests.

2.1.3 INSULATION WITHSTAND TEST EVALUATION

Table 5 lists the actual withstand voltage values. All values meet or exceed those listed in Table 2 of the above referenced standard section for all arrester ratings.

TABLE 5
Insulation Withstand Voltages

Arrester	Insulation	Withstand Vo	Itages
Rating	1.2/50 µs Impulse	1 min Dry	10 sec Wet
kV rms	kV crest	kV rms	kV rms
3	45	20	15
6	85	50	40
9	115	70	55
10	115	70	55
12	135	85	65
15	135	85	65
18	165	110	80
21	190	130	95
24	190	130	95
27	190	130	95
30	210	150	125
36	210	150	125

2.2 Discharge Voltage Characteristics

ANSI/IEEE C62.11-1987; Section 8.3

2.2.1 DISCHARGE VOLTAGE TEST DESCRIPTION

The discharge voltage tests serve to establish the relation between the voltage across the arrester terminals and the discharge current at several values of discharge current of specified waveshape.

2.2.2 DISCHARGE VOLTAGE TEST PROCEDURES

Discharge voltage measurements were made on new 9 kV arresters randomly selected and subjected to an 8/20 current wave at 1.5, 3, 5, 10, 20 and 40 kA. Each test was performed in compliance with the referenced section of the ANSI/IEEE C62.11-1987 standard.

Front-of-wave protective level tests were performed on new 9 kV, randomly selected, arresters. Each arrester was subjected to 8/20, 2/4 and 1/2 μ s current waves at 10 kA. A front-of-wave protective level corresponding to a discharge voltage cresting at 0.5 μ s was extrapolated from this data.

2.2.3 DISCHARGE VOLTAGE TEST EVALUATION

Figures 3 & 4 show typical test current and measured voltage oscillograms for discharge voltage tests at 10 and 20 kA.

Figure 5 shows a typical curve for determining the the front-of-wave protective level from discharge voltage versus time to voltage crest tests.

The curves in Figures 6 and 7 show the minimum and maximum discharge voltage versus impulse current for 10, 18 and 27 kV arrester ratings.

Tables 6 and 7 present the tabulated values for the minimum and maximum discharge voltage respectively and the front-of-wave protective level for each arrester rating.

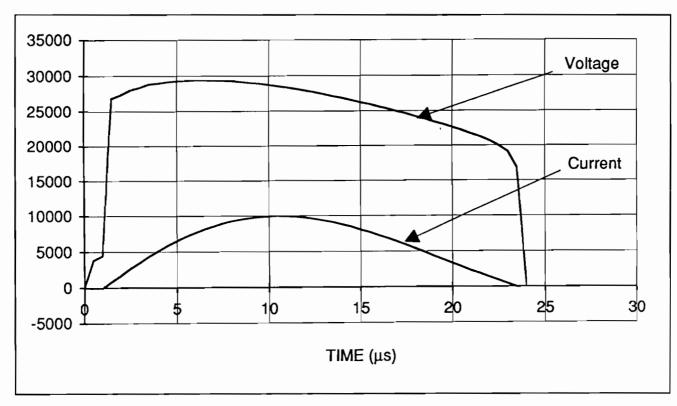


Figure 3 10 kA, 8/20 μs Discharge Voltage for AZL

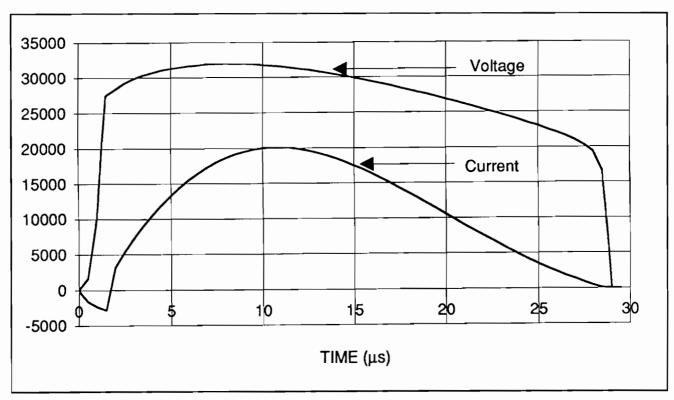


Figure 4 20 kA, 8/20 μs Discharge Voltage for AZL

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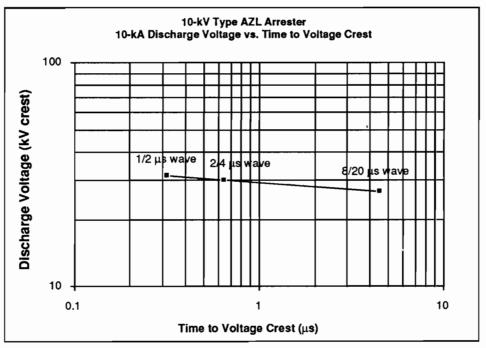


Figure 5

TABLE 6 Protective Characteristics-Minimum values

Arrester Rating	MCOV	Front-of-Wave Protective Level*		Minimum Discharge Voltage 8/20 μs Current Wave					
kV rms	kV rms	kV crest	1.5 kA	3 kA	5 kA	10 kA	20 kA	40 kA	kV crest
3	2.55	9.7	7.3	7.8	8.2	9.0	10.0	12.1	6.6
6	5.10	19.3	14.7	15.6	16.5	18.1	20.0	24.3	13.2
9	7.65	29.0	22.0	23.3	24.7	27.1	30.0	36.4	19.8
10	8.40	32.2	24.4	25.9	27.4	30.1	33.3	40.4	22.0
12	10.2	38.7	29.3	31.1	32.9	36.1	40.0	48.5	26.5
15	12.7	48.3	36.7	38.9	41.2	45.2	50.0	60.7	33.1
18	15.3	58.0	44.0	46.7	49.4	54.2	60.0	72.8	39.7
21	17.0	67.7	51.3	54.4	57.7	63.3	70.0	84.9	46.3
24	19.5	77.9	59.1	62.7	66.4	72.8	80.5	97.8	53.3
27	22.0	87.4	66.3	70.3	74.5	81.7	90.4	109.7	59.8
30	24.4	96.6	73.3	77.7	82.3	90.3	99.8	121.2	66.1

^{*}Based on a 10 kA current impulse that results in a discharge voltage cresting in 0.5 µs

Protective Characteristics-Maximum values

Arrester Rating	MCOV	Front-of-Wave Protective Level*	Maximum Discharge Voltage 8/20 μs Current Wave						
kV rms	kV rms	kV crest	1.5 kA	3 kA	5 kA	10 kA	20 kA	40 kA	kV crest
3	2.55	10.7	8.2	8.7	9.2	10.0	11.3	13.6	7.4
6	5.10	21.4	16.4	17.4	18.4	20.0	22.5	27.1	14.7
9	7.65	32.1	24.5	26.1	27.5	30.0	33.8	40.7	22.1
10	8.40	35.3	27.0	28.7	30.3	33.0	37.2	44.7	24.3
12	10.2	42.8	32.7	34.7	36.7	40.0	45.0	54.2	29.4
15	12.7	53.5	40.9	43.4	45.9	50.0	56.3	67.8	36.8
18	15.3	64.2	49.1	52.1	55.1	60.0	67.6	81.4	44.1
21	17.0	74.9	57.3	60.8	64.3	70.0	78.8	94.9	51.5
24	19.5	84.3	64.4	68.4	72.3	78.8	88.7	106.8	57.9
27	22.0	95.2	72.8	77.3	81.7	89.0	100.2	120.7	65.5
30	24.4	105.9	81.0	86.0	90.9	99.0	111.5	134.2	72.8

 $^{^{\}circ}$ Based on a 10 kA current impulse that results in a discharge voltage cresting in 0.5 μ s $^{\circ}$ 45-60 μ s rise time 500 A current surge.

^{**45-60} µs rise time 500 A current surge.

Section 2.2 (con't)

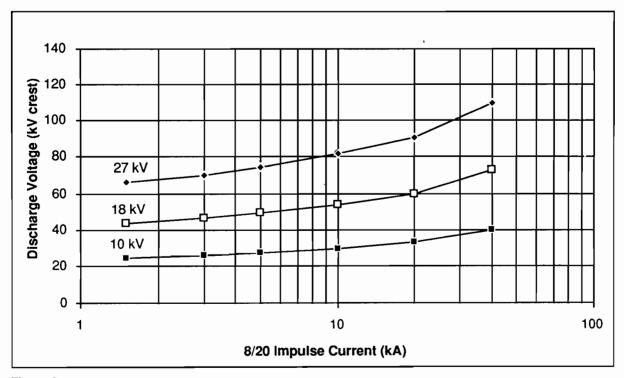


Figure 6 Minimum Discharge Voltage vs 8/20 Impulse Currents

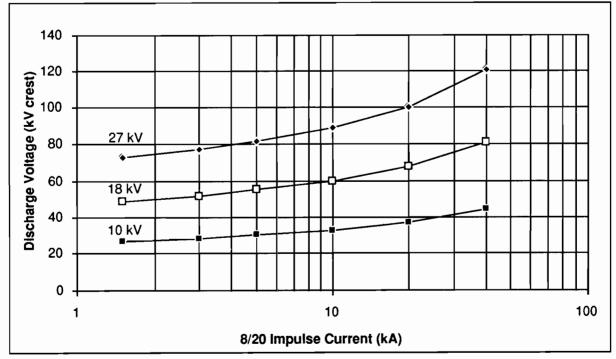


Figure 7
Maximum Discharge Voltage vs 8/20 Impulse Currents

2.3 Accelerated Aging Procedure

ANSI/IEEE C62.11-1987; Section 8.5

2.3.1 ACCELERATED AGING TEST DESCRIPTION

The accelerated aging test procedure provides a method to simulate the long-term effect of voltage and temperature on design parameters significant to the arrester performance. It is not a test in itself and has no evaluation procedure. It is an aging procedure from which voltage ratios are obtained for use in duty-cycle and discharge current withstand tests to simulate the performance of arresters as if they had been in service for an extended period equivalent to the test period of 1000 hrs. at 115 °C.

2.3.2 ACCELERATED AGING TEST PROCEDURE

Three typical AZL MOV elements were heated to 115 °C and energized at their MCOV for 1000 hrs. The maximum watts loss of each specimen was measured at MCOV and rated voltage during the first two to five hours of the test and at the conclusion of the 1000 hours of continuous testing.

2.3.3 ACCELERATED AGING TEST EVALUATION

If the voltage ratios determined during this test are greater than 1.0, then the voltages used for duty-cycle and discharge current withstand tests must be adjusted accordingly.

Since all final watts loss values were lower than the initial watts loss values the voltage ratios (Kc & Kr) are equal to 1.0, therefore no test voltage adjustments need to be made for duty-cycle or discharge current withstand tests.

Figure 8 illustrates the watts loss curves for the three AZL MOV elements at MCOV for the 1000 hr. duration of the accelerated aging test. The data is also tabulated in Table 8.

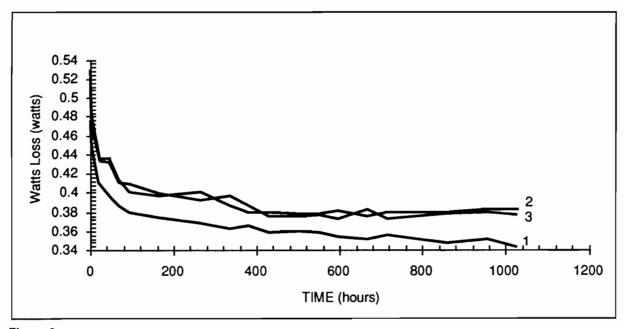


Figure 8
Typical Accelerated Aging Test at MCOV - Watts Loss vs. Time at 115 °C

TABLE 8
Accelerated Aging Test Summary at 115 °C

Test Voltage		MCOV			Rated			
Disk Number	1	2	3	1	2	3		
Watts @ 2.5 hr.	.45	.48	.47	1.2	1.3	1.3		
Watts @ 1000 hr.	.34	.38	.38	.86	.98	.98		
		Kc=1						
K Value					Kr=1			

Section 2.4 Discharge-Current Withstand Tests

ANSI/IEEE C62.11-1987; Section 8.6

2.4.1 DISCHARGE-CURRENT WITHSTAND TEST DESCRIPTION

The discharge current withstand tests, consisting of high-current, short-duration and low-current, long-duration tests, serve to demonstrate the adequacy of the electrical, mechanical and thermal design of the arrester.

2.4.2 DISCHARGE-CURRENT WITHSTAND TEST PROCEDURES

2.4.2.1 High-Current, Short-Duration

Three 9 kV AZL arresters were subjected to two 4/10 µs impulse current waves having 100 kA-crest amplitude. Within five minutes of the second discharge each arrester was energized at MCOV and the power monitored for thirty (30) minutes as prescribed in Section 8.6.1.2 of the referenced standard. An applied voltage greater than MCOV was not necessary since the voltage ratios determined in the accelerated aging tests were less than 1.0.

The discharge voltage of each arrester measured with a 10 kA-crest, 8/20 µs current wave was determined before and after the high-current, short-duration current withstand test.

2.4.2.2 Low-Current, Long-Duration

Three 9 kV AZL arresters were subjected to a minimum 250 A 2000 µs rectangular current wave. The test current waves were applied in three groups of six operations followed by one group of two operations for a total of twenty (20) operations with a one minute interval between successive operations throughout the test. Specimens were allowed to cool to ambient temperature between the groups of operations. Prior to the 19th operation the arrester temperature was equilibrated at 60 °C. Within five minutes of the 20th operation the arrester was energized at MCOV and the power monitored for thirty (30) minutes as prescribed in Section 8.6.2.1.3 of the referenced standard. An applied voltage greater than MCOV was not necessary since the voltage ratios determined in the accelerated aging tests were less than 1.0.

The discharge voltage of each arrester measured with a 10 kA-crest, 8/20 µs current wave was determined before and after the low-current, long-duration withstand test.

2.4.3 DISCHARGE-CURRENT WITHSTAND TEST EVALUATION

2.4.3.1 High-Current, Short-Duration

The oscilligrams in Figures 9 and 10 show the typical pairs of current and voltage traces for the first and second high current, short-duration operations for a 9 kV AZL surge arrester.

The arresters pass this design test based on the performance criteria listed below:

- 1. Each arrester exhibited thermal recovery by demonstrating continuously decreasing power values over the thirty (30) minute monitoring after the current withstand test. This is illustrated in Table 9.
- 2. There was no evidence of physical or electrical deterioration caused by the current withstand tests.
- 3. The 10 kA (8/20 μs) discharge voltage changed by less than 10% as shown in Table 9.

TABLE 9
High-Current, Short-Duration Test Summary

Arrester	100 kA Discharge Voltage(4/10 μs)		10 kA Dis Voltage(8		Watts Loss @ MCOV		
Rating	1st shot	2nd shot	Before 100 kA	After 100 kA	Initial	30 Min.	
kV rms	kV crest	kV crest	kV crest	kV crest	Watts	Watts	
9	54.1	50.9	29.3	28.8	7.4	1.1	
9	54.1	52.7	29.2	28.8	5.8	.88	
9	54.7	54.1	29.3	29.1	6.2	1.1	

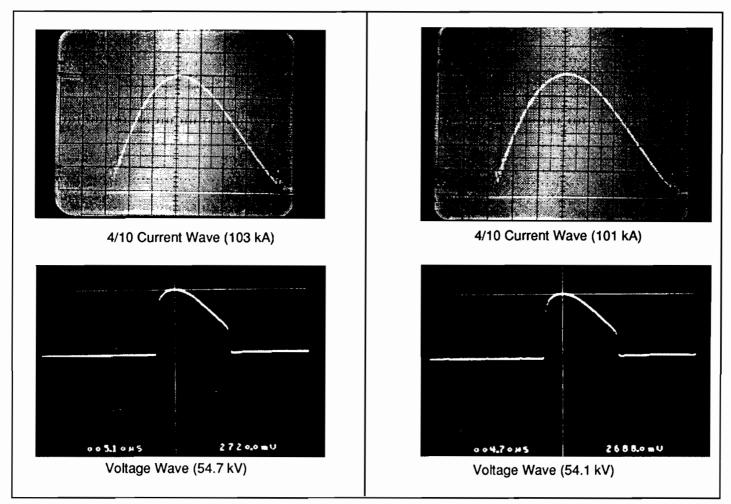


Figure 9
1st High-Current, Short Duration (100kA) operation

Figure 10 2nd High-Current, Short Duration (100kA) operation

2.4.3.2 Low-Current, Long-Duration

Typical oscilligrams in Figures 11 and 12 show the corresponding pairs of current and voltage traces for the first and twentieth low-current, long-duration operations for a 9 kV AZL surge arrester.

The arresters pass this design test based on the performance criteria listed below:

- 1. Each arrester exhibited thermal recovery by demonstrating continuously decreasing power values over the thirty (30) minute monitoring after the current withstand test. This is illustrated in Table 10.
- 2. There was no evidence of physical or electrical deterioration caused by the current withstand tests.
- 3. The 10 kA $(8/20\,\mu s)$ discharge voltages measured after the low-current, long-duration test remained essentially unchanged from the initial values as shown in Table 10.

TABLE 10
Low-Current, Long-Duration Test Summary

Arrester	2	250 A 2000 μs		10 kA Dischar	ge Voltage	Watts Loss @ MCOV		
Rating	1st	19th *	20th	(8/20 μs) Before LCLD After LCLD		Initial	30 Min.	
	Discharge	Discharge	Discharge					
kV rms	kV crest	kV crest	kV crest	kV Crest	kV Crest	Watts	Watts	
9	20.4	20.6	20.8	29.3	29.5	2.2	.56	
9	20.5	20.6	20.7	29.4	29.3	2.3	.55	
9	20.4	20.6	20.7	29.3	29.2	2.4	.62	

^{*-} Temperature was equilibrated at 60 °C before the 19th discharge

Section 2.4 (con't)

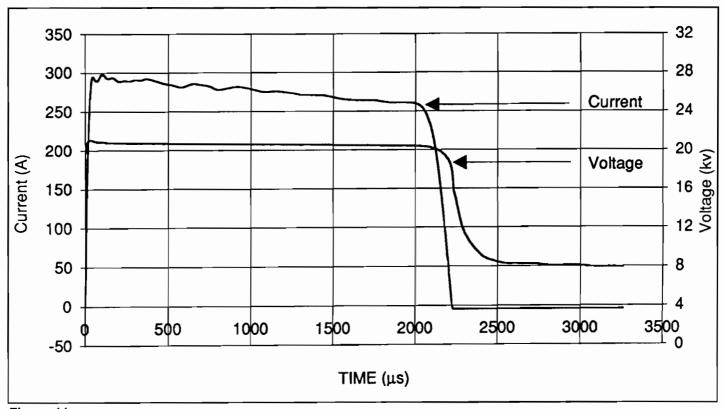


Figure 11 $_{1^{st}}$ Low-Current, Long Duration Operation (250 A 2000 $\mu s)$

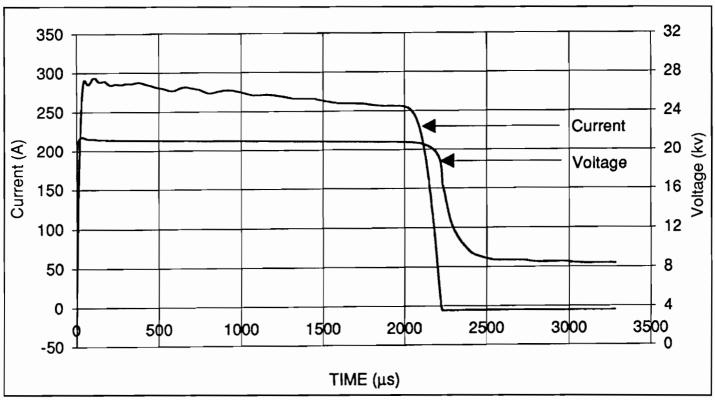


Figure 12 20^{th} Low-Current, Long Duration Operation (250 A 2000 μ s)

2.5 Duty-Cycle Test

ANSI/IEEE C62.11-1987; Section 8.7

2.5.1 DUTY-CYCLE TEST DESCRIPTION

The duty-cycle test serves to establish the ability of the arrester to discharge impulse current repeatedly while energized at duty-cycle voltage and thermally recover at MCOV.

2.5.2 DUTY-CYCLE TEST PROCEDURE

Three 9 kV AZL prorated sections were subjected to twenty (20) operations of a 10 kA- crest, 8/20 µs current wave while being continuously energized at rated power frequency voltage. The current wave initiation was approximately 60 degrees before the crest of the power frequency voltage and was applied at one minute intervals.

Following the twentieth operation each section was energized at rated voltage and subjected to two 40 kA, 8/20 µs current waves with a one minute interval between operations. After the 40 kA operations the arrester remained energized at rated voltage and the power was monitored for thirty (30) minutes.

The 10 kA-crest, $8/20~\mu s$ discharge voltage was measured before the operating duty-cycle test and again when the sections had returned to ambient temperature subsequent to the operating duty-cycle test.

2.5.3 DUTY-CYCLE TEST EVALUATION

Figures 13 and 14 show typical oscillograms of the corresponding pairs of the current and voltage traces after the first and twentieth 10 kA duty-cycle operations respectively.

Figures 15 and 16 present similar data for after each of the 40 kA duty-cycle operations.

The data presented in the above figures is also in Table 11 for both the 10 kA and 40 kA duty-cycle tests.

The arresters pass this design test based on the performance criteria listed below:

- 1. Each section exhibited thermal recovery by demonstrating continuously decreasing power values over the thirty (30) minute monitoring after the operating duty-cycle test. This is illustrated in Table 11.
- 2. There was no evidence of physical or electrical deterioration caused by the operating duty-cycle test.
- 3. The 10 kA (8/20 μs) discharge voltages measured after the duty-cycle test remained essentially unchanged from the initial values as shown in Table 11.

TABLE 11
Operating Duty-Cycle Test Summary

Arrester Rating	20, 10kA Discharges at ODC Rating Watts Loss				kA Discharge at ODC rating Watts Loss	10 kA Discharge Voltage (8/20 μs)		
	1st 20th			21st	22nd	After	Before ODC	After ODC
kV rms	Initial	Discharge	Discharge	Discharge	Discharge	30 min.	kV crest	kV crest
9	.65	.84	6.5	1.2	3.5	.38	29.2	29.4
9	.70	.84	6.6	1.2	3.2	.43	29.1	29.1
9	.66	.80	7.1	1.3	3.4	.32	28.9	29.1

Section 2.5 (con't)

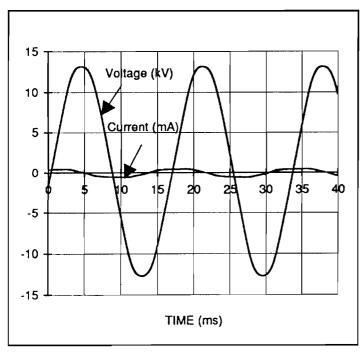


Figure 13
1st Operating Duty-Cycle Operation

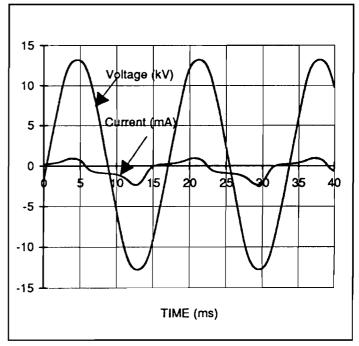


Figure 14 20th Operating Duty-Cycle Operation

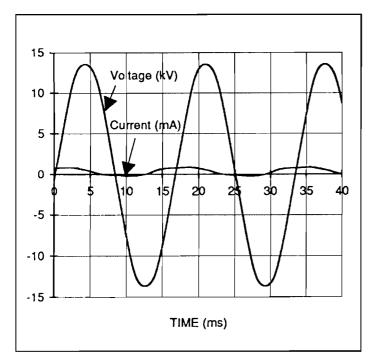


Figure 15 21st Operating Duty-Cycle Operation

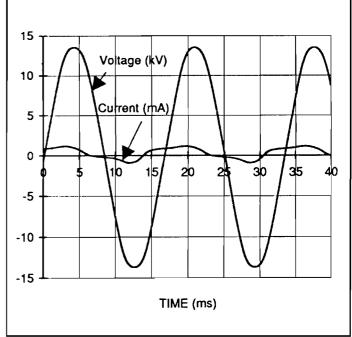


Figure 16 22nd Operating Duty-Cycle Operation

2.6 Internal-Ionization Voltage and Radio-Influence Voltage

ANSI/IEEE C62.11-1987; Section 8.8 and NEMA LA-1-1986

2.6.1 INTERNAL-IONIZATION VOLTAGE AND RADIO-INFLUENCE VOLTAGE TEST DESCRIPTION

The internal-ionization voltage test provides a measure of ionization current present within an arrester design. This may cause deterioration of internal arrester parts. The radio-influence voltage (RIV) test provides a measure of externally generated "noise" which would be a nuisance to communication equipment.

2.6.2 INTERNAL-IONIZATION VOLTAGE AND RADIO-INFLUENCE VOLTAGE TEST PROCEDURES

Arresters of each rating were selected and energized at the specified voltage and the internal-ionization voltage and RIV measured.

2.6.3 INTERNAL-IONIZATION VOLTAGE AND RADIO-INFLUENCE VOLTAGE TEST EVALUATION

The measured internal-ionization voltage and RIV values do not normally exceed ambient background values and therefore are less than the maximum specified values listed in Table 12.

TABLE 12
Internal-Ionization and Radio-Influence Voltages

Arrester Rating	Test Voltage	Max. Spec. Internal Ionization	Max. Spec. RIV
kV rms	kV rms	μV	μ V
3	3.54	50	250
6	7.07	50	250
9	10.6	50	250
10	11.8	50	250
12	14.1	50	250
15	17.7	50	250
18	21.2	50	250
21	24.7	50	250
24	28.3	50	250
27	31.8	50	250
30	35.3	50	250
36	42.4	50	250

The AZL arrester is 100% tested at the prescribed voltage. The total RIV for a typical arrester does not normally exceed ambient background values.

2.7 Distribution Arrester Disconnector Tests

ANSI/IEEE C62.11-1987; Section 8.11

2.7.1 DISTRIBUTION ARRESTER DISCONNECTOR TEST DESCRIPTION

The disconnector tests serve to demonstrate that the disconnectors can withstand, without operation, the arrester design tests and provide a current-time characteristic operating curve.

2.7.2 DISTRIBUTION ARRESTER DISCONNECTOR TEST PROCEDURES

In accordance with Sections 8.11.2.1 and 8.11.2.2 of the above referenced standard the ground lead disconnector successfully withstood, without operating, the following distribution arrester tests:

- 1. High-current, short-duration current withstand tests (8.6.1)
- 2. Low-current, long-duration current withstand tests (8.6.2)
- 3. Operating duty-cycle tests (8.7.1)
- 4. Contamination tests (8.12)

Separate tests were performed according to Section 8.11.2.3 to establish a time-current characteristic required to cause the disconnector to operate. Data for this section of the tests was collected at 20, 80, 200, and 800 A rms.

Section 2.7 (con't)

2.7.3 DISTRIBUTION ARRESTER DISCONNECTOR TEST EVALUATION

Figure 17 presents the resultant time-current characteristics for the Cooper Power Systems distribution arrester disconnector.

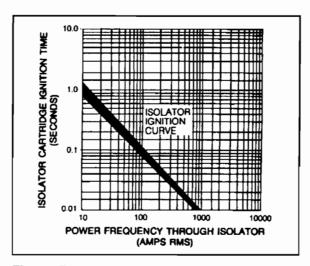


Figure 17
Distribution Arrester Disconnector
Time-Current Characteristic Plot

2.8 Contamination Tests

ANSI/IEEE C62.11-1987; Section 8.12

2.8.1 CONTAMINATION TEST DESCRIPTION

The contamination tests serve to demonstrate that the arrester will withstand a prescribed power-frequency voltage under prescribed surface contamination without flashover or thermal runaway.

2.8.2 CONTAMINATION TEST PROCEDURES

The arresters were subjected to two, fifteen (15) minute cycles with MCOV applied to the arrester per Section 8.12.3. Contaminant slurry was applied, employing the application method prescribed by Section 8.12, prior to the start of each cycle. Stability was monitored at the end of the second cycle.

2.8.3 CONTAMINATION TEST EVALUATION

Watts loss and the resistive component of current were monitored continuously during the test. The test was deemed successful since all samples remained thermally stable and none flashed over or showed signs of physical damage to internal parts.

2.9 Temporary Overvoltage Tests

2.9.1 TEMPORARY OVERVOLTAGE TEST DESCRIPTION

Although this is not a test prescribed or described by the ANSI/IEEE standard, it is none the less an important test to establish a power frequency voltage-time curve that demonstrates an arresters ability to successfully discharge current, while experiencing an overvoltage excursion in power frequency voltage, without thermal run away.

2.9.2 TEMPORARY OVERVOLTAGE TEST PROCEDURES

The temporary overvoltage capability of the Type AZL Distribution Arrester is shown by the curve in Figure 18. The curve was established by applying power frequency voltages to preheated (to 60 °C) test samples, above their duty cycle rating, for various times between 0.1 and 10,000 seconds. The test overvoltage was reduced to the sample MCOV, within 200 ms, prior to thermal run-away failure. The area under the curve is the voltage-time characteristic in which the samples demonstrated thermal stability during a thirty (30) minute monitoring period, with a final watts-loss equal to or less than their original value.

2.9.3 TEMPORARY OVERVOLTAGE TEST EVALUATION

Figure 18 illustrates the temporary overvoltage capability of the McGraw-Edison VariSTAR® Type AZL Arrester.

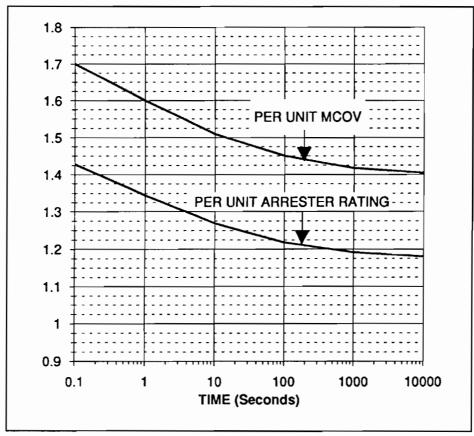


Figure 18
Temporary Overvoltage Capability McGraw-Edison VariSTAR® Type AZL Arrester.

NOTE: To enhance the readers understanding of results, many of the scope traces generated during testing were digitized and transferred to a spreadsheet. All such graphs were generated from unedited raw data.



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