MFBA3V2012

Automotive high impedance multilayer chip ferrite bead



Product features

- · AEC-Q200
- · 0805 (2012 metric) surface mount package
- · Impedance range 11 ohms to 2000 ohms
- Multilayer monolithic construction yields high reliability
- · Moisture sensitivity level (MSL): 1

Applications

- Body electronics (keyless entry, ECU, antennas)
- Advanced driver assistance systems (ADAS)
- Infotainment and cluster electronics
- · Safety electronics systems
- WLAN, WiFi, Bluetooth
- Portable medical devices
- Inventory management equipment
- Displays/monitors
- · IoT, remote monitoring
- Testing equipment
- Automation equipment
- Sensors

Environmental compliance and general specifications

- Operating temperature range: -55 °C to +150 °C (ambient plus self-temperature rise)
- Storage temperature (component): -55 °C to +150 °C
- Solder reflow temperature:
 J-STD-020 (latest revision) compliant









Product specifications

Part number ²	Impedance (Ω) 100 MHz, ±25%, @ +25°C	DCR (Ω) maximum @ +25 °C	Rated current¹ (mA) maximum
MFBA3V2012K-110-R	11	0.1	900
MFBA3V2012K-260-R	26	0.1	600
MFBA3V2012K-300-R	30	0.1	600
MFBA3V2012K-400-R	40	0.1	600
MFBA3V2012K-600-R	60	0.1	900
MFBA3V2012K-121-R	120	0.2	800
MFBA3V2012K-151-R	150	0.2	800
MFBA3V2012K-221-R	220	0.3	750
MFBA3V2012K-301-R	300	0.3	700
MFBA3V2012K-601-R	600	0.4	500
MFBA3V2012K-102-R	1000	0.45	400
MFBA3V2012H-152-R	1500	0.5	350
MFBA3V2012H-202-R	2000	0.6	400

^{1.} Rated current: DC current for an approximate temperature rise of 40 °C without core loss.

MFBA3V2012y = Product code and size (y=Internal code)

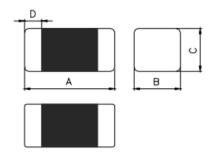
xxx =Impedance value in Ω , last character equals number of zeros

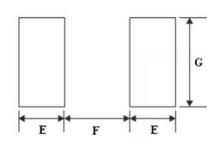
-R suffix = RoHS compliant

Mechanical parameters (mm)

Recommended pad layout

Schematic







Part number	Α	В	С	D	E	F	G
MFBA3V2012y-xxx-R	2.00 ± 0.20	1.25 ± 0.20	0.85 ± 0.20	0.50 ± 0.30	1.05	1.00	1.45

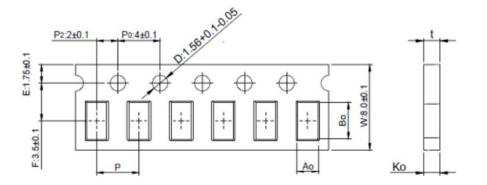
Part marking: No marking All soldering surfaces to be coplanar within 0.1 millimeters Tolerances are ±0.1 millimeters unless stated otherwise Pad layout dimensions are reference only Traces or vias underneath the inductor is not recommended

^{2.} Part number definition: MFBA3V2012y-xxx-R

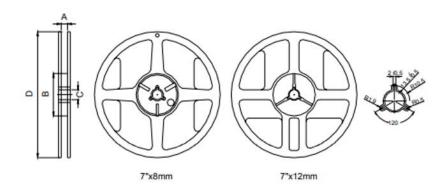
Packaging information (mm)

Drawing not to scale

Supplied in tape and reel packaging, 4000 parts per 7" diameter reel (EIA-481 compliant)

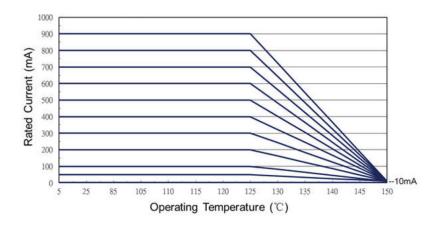


Во	2.10 ± 0.05
Ao	1.30 ± 0.05
Ко	0.95 ± 0.05
Р	4.0 ± 0.10
t	0.95 ± 0.05



Туре	7″*8	
Α	9.0 ± 0.5	
В	60 ± 2	_
C	13.5 ± 0.5	_
D	178 ± 2	

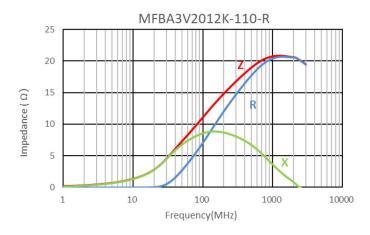
Derating curve for rated current < 1000 mA

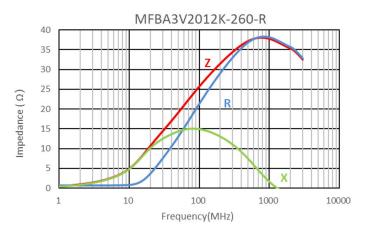


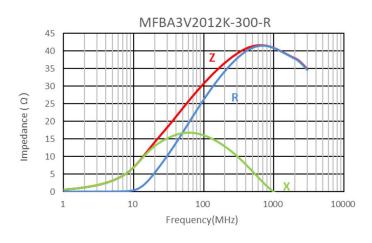
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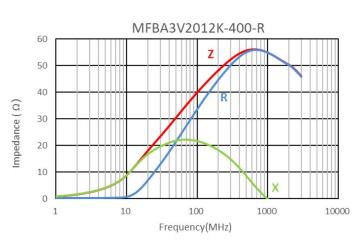
Impedance vs frequency

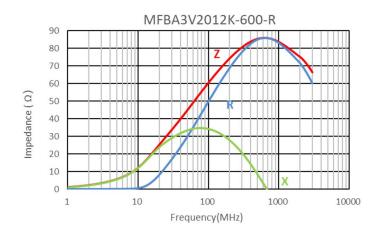
Z=Impedance, R=Resistance, X=Reactance

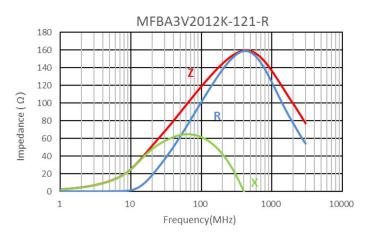






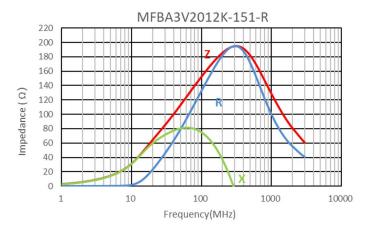


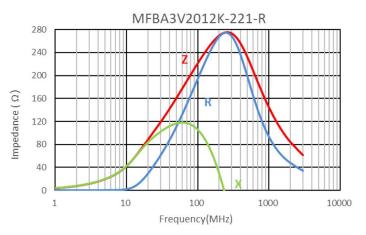


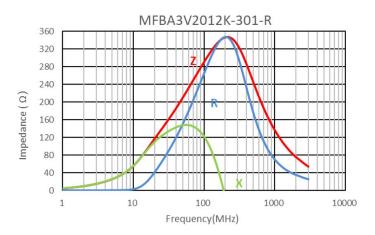


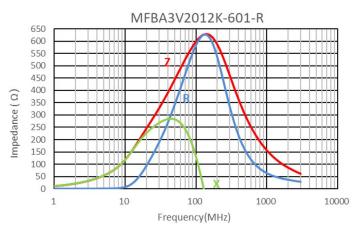
Impedance vs frequency, continued

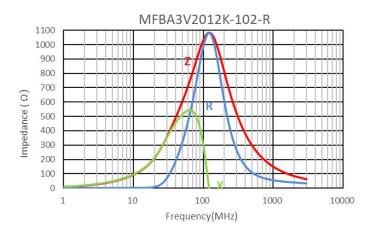
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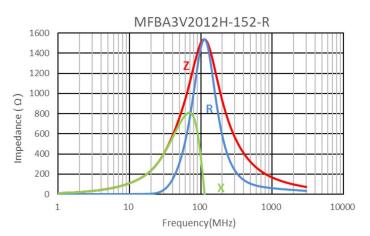






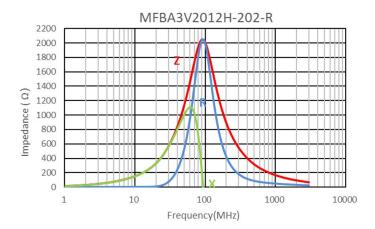






Impedance vs frequency

Z=Impedance, R=Resistance, X=Reactance



Solder reflow profile

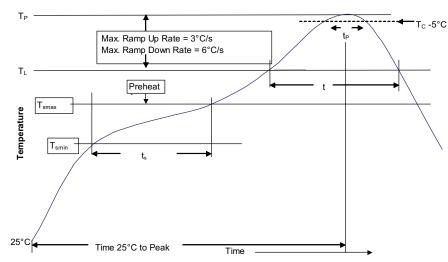


Table 1 - Standard SnPb solder (T_C)

Package thickness	Volume mm3 <350	Volume mm3 ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2 - Lead (Pb) free solder (T_C)

Package thickness	Volume mm³ <350	Volume mm³ 350 - 2000	Volume mm³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 – 2.5 mm	260 °C	250 °C	245 °C
>2.5 mm	250 °C	245 °C	245 °C

Reference J-STD-020

Profile feature	Standard SnPb solder	Lead (Pb) free solder
Preheat and soak • Temperature min. (T _{smin})	100 °C	150 °C
• Temperature max. (T _{smax})	150 °C	200 °C
• Time (T _{smin} to T _{smax}) (t _s)	60-120 seconds	60-120 seconds
Ramp up rate T_L to T_p	3 °C/ second max.	3 °C/ second max.
Liquidous temperature (TL) Time (t_L) maintained above T_L	183 °C 60-150 seconds	217 °C 60-150 seconds
Peak package body temperature (Tp)*	Table 1	Table 2
Time (t _p)* within 5 °C of the specified classification temperature (T _C)	20 seconds*	30 seconds*
Ramp-down rate (T _p to T _L)	6 °C/ second max.	6 °C/ second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

 $^{^{\}star}$ Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.

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