



Version: 3.9

IL02602008E Build 59942

© 2023

© 2023 Eaton Corporation. All rights reserved.

Eaton Corporation

Electrical Sector

1000 Eaton Boulevard

Cleveland, OH 44122

United States

Eaton.com

Eaton Technical Support

Phone: 877-ETN-CARE (877-386-2273) option 2 Sub-option 5,1,1

844-435-8982

Email: MRSupport@eaton.com

Table of Contents

1	Modbus Parameters	4
1.1	Notes for the SCADA-System	5
2	Specific Modbus Function Codes	6
2.1	Function Code 3/4	7
2.2	Function Code 5	8
2.3	Function Code 8	9
2.4	Function Code 16	10
2.5	Setting Date and Time	11
2.6	Supported MODBUS Error Messages	12
3	Appendix - Register Maps	13
3.1	Signals	13
3.2	Measuring Values	65
3.3	Commands	69
3.4	Settings	71
3.5	Cause of Trip	73
3.6	Fast Status Register	74

1 Modbus Parameters

For the Modbus Protocol several parameters have to be set which are relevant for the communication between the control system (SCADA) and the device. The parameters and their setting possibilities or value ranges are shown in the tables below.

NOTICE!



The Parameters are described within the Reference Manual of the device (separate document).

1.1 Notes for the SCADA-System

When using Modbus RTU the following times have to be considered by the control system and are fixed within the device:

The dwell times (t_D) before start of a telegram must be set at least to 3.5 characters.

Examples:

- 3.5 characters 9600 Baud = 4 ms
- 3.5 characters 19200 Baud = 2 ms
- 3.5 characters 38400 Baud = 1 ms

Start of a new telegram is expected when the dwell time (t_D) is > 3.5 characters.

The fact that the probability of disruptions during transmission of a telegram increases with its length has to be taken into duly consideration and thus a query to the Slave should be possibly such that the response telegram is not much longer than 32 Bytes.

2 Specific Modbus Function Codes

For reading out data from the device or to carry out commands, the services listed in the table, also called »Function Codes«, are supported.

Function Code	Designation	Description
3	Read Holding Registers	There are single or several data words read as from a specific data word address. Only status addresses and parameter addresses can be read.
4	Read Input Registers	There are single or several data words read as from a specific data word address. Only measuring values can be read.
5	Write single Output (Bit)	All other values are illegal and will not affect the output. Via this function code acknowledgments can be executed as well as counters reseted or blockings set.
8	Loopback Test	Test function for the communication system.
16	Load Multiple Registers	There are single or several data words written as from a specific data word address.

On the following pages the Modbus functions are described in detail.

2.1 Function Code 3/4

Query

Slave address	3/4	Register address	Register address	Register number	Register number	Check-sum	Check-sum
		HI	LO	HI	LO	HI	LO

Response

Slave address	3/4	Byte number	Register 0	Register 0	...	Check-sum	Check-sum
			HI	LO		HI	LO

Register address (HI · 256 + LO) — The data word address from where reading should start.

Register number (HI · 256 + LO) — Number of data words to be read. Valid range: 1...125

Byte number — Number of subsequent Bytes containing data words.

Register — Data words read out of the device (Highbyte and Lowbyte).

2.2 Function Code 5

Query

Slave address	5	Register address	Register address	Register data	Register data	Check-sum	Check-sum
		HI	LO	HI	LO	HI	LO

Response

Slave address	5	Register address	Register address	Register data	Register data	Check-sum	Check-sum
		HI	LO	HI	LO	HI	LO

Register address (HI · 256 + LO) — Data word address to be written

Register data — Value of the data word to be written (High-byte and Low-byte).

Permitted value range:

- FF00 hex request for a single bit to be on: This often means to reset a counter, execute acknowledgments or set blocking signals.
- 0000 hex request for a single bit to be off: This often means to deactivate blocking signals or to reset single bits.

2.3 Function Code 8

Query

Slave address	8	Data Diag	Data Diag	Test data	Test data	Check-sum	Check-sum
		Code HI	Code LO			HI	LO
		0x00	0x00				

Response

Slave address	8	Data Diag	Data Diag	Test data	Test data	Check-sum	Check-sum
		Code HI	Code LO			HI	LO

Data Diag Code HI (high), Data Diag Code LO (Low) — Diagnostic Code (subfunction code of function code 8) for testing the communication system. The Diagnostic Code “Return Query Data” (0x00, 0x00) is being supported.

Test Data — By using the Diagnostic Code 0x00 0x00, the transmitted data is sent back to the Master unchanged.

2.4 Function Code 16

Query

Slave address	16	Register address	Register address	Register number	Register number	Byte number	Register 0	Register 0	...	Check-sum	Check-sum
		HI	LO	HI	LO		HI	LO		HI	LO

Response

Slave address	16	Register address	Register address	Register number	Register number	Check-sum	Check-sum
		HI	LO	HI	LO	HI	LO

Register address (HI · 256 + LO) — Data word address as from where writing should start.

Register number (HI · 256 + LO):

- Query: Number of data words to be written. Valid range: 1...123.
- Response: Number of data words written.

Byte number — Number of subsequent Bytes to contain data words.

Register — Data words read out of the device (High-byte and Low-byte).

2.5 Setting Date and Time

Date and time can be set by means of function code 16 and read with function code 3. If the device address 0 (broadcast address) is selected, the times of all devices connected to this bus are simultaneously reset.

NOTICE!



The devices do not respond to a broadcast command.

2.6 Supported MODBUS Error Messages

Exception Response Telegrams are described within the general "Modbus Application Protocol Specification". An exception response table with examples is shown there. The table below contains just the actually used codes. In case the device has recognized an error it will react in the following way:

Exception Code	Designation	Description
1	Illegal Function	The message received includes a function code which is not supported by the Slave.
2	Illegal Data Address	Access was sought on a data word address not included in the data module.
3	Illegal Data Value	The received message contains an invalid data structure (e. g. wrong number of data bytes).
4	Slave Device Failure	An unrecoverable error occurred while the server (or slave) was attempting to perform the requested action.

The response given by the *device* in case of a failure has the following format:

Slave Address	0x80	Exception Code	Check-sum	Check-sum
	+ Function Code		HI	LO

In the second Byte of the response the Function Code is sent with the highest Bit set to 1. This is equivalent to an addition by 0x80. The third Byte holds the Exception Code of the error message.

3 Appendix - Register Maps

3.1 Signals

Legend: (*) = These signals have to be acknowledged by the Scada System.

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
86[1] - Lockout Relais 86 TRIP		177	1	3	Struct			
	Cin NO-I	177	1	3	Bit	0x1 (1)	-	Module input state: This is the feedback signal indicating that the Lockout Relay is in the TRIPPED position. (This is normally implemented by using the NO contact.)
	Cin NC-I	177	1	3	Bit	0x2 (2)	-	Module input state: This is the feedback signal indicating that the Lockout Relay is in the RESET position. (This is normally implemented by using the NC contact.)
	SC TRIP-I	177	1	3	Bit	0x800 (12)	-	State of the module input: Switch Command to switch the Lockout-Relay to TRIPPED, e.g. via digital input or logic.
	SC RESET-I	177	1	3	Bit	0x1000 (13)	-	State of the module input: Switch Command to RESET the Lockout-Relay, e.g. via digital input or logic.
	TripCmd (*)	177	1	3	Bit	0x2000 (14)	-	Signal: Trip Command
	Manual TRIP Cmd	177	1	3	Bit	0x4000 (15)	-	Signal: Command to switch the Lockout-Relay to TRIPPED.
86[1] - Lockout Relais 86 TRIP		178	1	3	Struct			

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Manual RESET Cmd	178	1	3	Bit	0x1 (1)	-	Signal: Command to RESET the Lockout-Relay.
	CES Disturbed	178	1	3	Bit	0x40 (7)	-	Signal: Command Execution Supervision: Switching Command unsuccessful. The Lockout Relay is in disturbed position.
	CES RESET d. TRIP	178	1	3	Bit	0x400 (11)	-	Signal: Command Execution Supervision: CLOSE Command during a pending OPEN Command.
	CES SwitchDir	178	1	3	Bit	0x800 (12)	-	Signal: Command Execution Supervision / Switching Direction Control: This signal becomes true if a command is issued to the Lockout Relay even though it is already in the requested position. Example: The Lockout Relay shall be switched to TRIPPED, but it is already in the TRIPPED position.
	CES succesf	178	1	3	Bit	0x4000 (15)	-	Signal: Command Execution Supervision: Switching command executed successfully.
86[1] – Lockout Relais 86 TRIP		179	1	3	Struct			
	Pos Disturb	179	1	3	Bit	0x1 (1)	-	Signal: Lockout Relay DISTURBED. (The position indicators contradict each other. This signal becomes true after the supervision timer t-Move RESET/t-Move TRIPPED has expired.)
	Pos Indeterm	179	1	3	Bit	0x4 (3)	-	Signal: Lockout Relay is in Indeterminate Position.
	Pos TRIPPED	179	1	3	Bit	0x8 (4)	-	Signal: The Lockout Relay is in TRIPPED position

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Pos RESET	179	1	3	Bit	0x10 (5)	-	Signal: The Lockout Relay is in RESET position
	Pos not RESET	179	1	3	Bit	0x40 (7)	-	The Lockout Relay is not in the RESET position. This means that it is either "TRIPPED" or "moving" or "disturbed".
	SI SingleContactInd	179	1	3	Bit	0x80 (8)	-	Signal: The state of the Lockout Relay is detected by one auxiliary contact (pole) only. Therefore indeterminate and disturbed states cannot be detected.
	CES Fail TripCmd	179	1	3	Bit	0x800 (12)	-	Signal: Command Execution Supervision: Command execution failed because trip command is pending.
86[1] - Lockout Relais 86 TRIP		195	1	3	Struct			
	Operations Alarm	195	1	3	Bit	0x100 (9)	-	Signal: Too many Operations. (The operations counter »TripCmd Cr« has exceeded the limit set at »Operations Alarm«.)
86[2] - Lockout Relais 86CT		180	1	3	Struct			
	Cin NO-I	180	1	3	Bit	0x1 (1)	-	Module input state: This is the feedback signal indicating that the Lockout Relay is in the TRIPPED position. (This is normally implemented by using the NO contact.)
	Cin NC-I	180	1	3	Bit	0x2 (2)	-	Module input state: This is the feedback signal indicating that the Lockout Relay is in the RESET position. (This is normally implemented by using the NC contact.)
	SC TRIP-I	180	1	3	Bit	0x800 (12)	-	State of the module input: Switch Command to switch the Lockout-Relay to TRIPPED, e.g. via digital input or logic.

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	SC RESET-I	180	1	3	Bit	0x1000 (13)	-	State of the module input: Switch Command to RESET the Lockout-Relay, e.g. via digital input or logic.
	TripCmd (*)	180	1	3	Bit	0x2000 (14)	-	Signal: Trip Command
	Manual TRIP Cmd	180	1	3	Bit	0x4000 (15)	-	Signal: Command to switch the Lockout-Relay to TRIPPED.
86[2] - Lockout Relais 86CT		181	1	3	Struct			
	Manual RESET Cmd	181	1	3	Bit	0x1 (1)	-	Signal: Command to RESET the Lockout-Relay.
	CES Disturbed	181	1	3	Bit	0x40 (7)	-	Signal: Command Execution Supervision: Switching Command unsuccessful. The Lockout Relay is in disturbed position.
	CES RESET d. TRIP	181	1	3	Bit	0x400 (11)	-	Signal: Command Execution Supervision: CLOSE Command during a pending OPEN Command.
	CES SwitchDir	181	1	3	Bit	0x800 (12)	-	Signal: Command Execution Supervision / Switching Direction Control: This signal becomes true if a command is issued to the Lockout Relay even though it is already in the requested position. Example: The Lockout Relay shall be switched to TRIPPED, but it is already in the TRIPPED position.
	CES succesf	181	1	3	Bit	0x4000 (15)	-	Signal: Command Execution Supervision: Switching command executed successfully.
86[2] - Lockout Relais 86CT		182	1	3	Struct			

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Pos Disturb	182	1	3	Bit	0x1 (1)	-	Signal: Lockout Relay DISTURBED. (The position indicators contradict each other. This signal becomes true after the supervision timer t-Move RESET/t-Move TRIPPED has expired.)
	Pos Indeterm	182	1	3	Bit	0x4 (3)	-	Signal: Lockout Relay is in Indeterminate Position.
	Pos TRIPPED	182	1	3	Bit	0x8 (4)	-	Signal: The Lockout Relay is in TRIPPED position
	Pos RESET	182	1	3	Bit	0x10 (5)	-	Signal: The Lockout Relay is in RESET position
	Pos not RESET	182	1	3	Bit	0x40 (7)	-	The Lockout Relay is not in the RESET position. This means that it is either "TRIPPED" or "moving" or "disturbed".
	SI SingleContactInd	182	1	3	Bit	0x80 (8)	-	Signal: The state of the Lockout Relay is detected by one auxiliary contact (pole) only. Therefore indeterminate and disturbed states cannot be detected.
	CES Fail TripCmd	182	1	3	Bit	0x800 (12)	-	Signal: Command Execution Supervision: Command execution failed because trip command is pending.
86[2] - Lockout Relais 86CT		196	1	3	Struct			
	Operations Alarm	196	1	3	Bit	0x100 (9)	-	Signal: Too many Operations. (The operations counter »TripCmd Cr« has exceeded the limit set at »Operations Alarm«.)
87SV[1] - Open CT Supervision		295	1	3	Struct			
	ExBlo1-I	295	1	3	Bit	0x1 (1)	-	Module Input State: External Blocking1

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	ExBlo2-I	295	1	3	Bit	0x2 (2)	-	Module Input State: External Blocking2
	Active	295	1	3	Bit	0x4 (3)	-	Signal: Active
	ExBlo	295	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Pickup	295	1	3	Bit	0x10 (5)	-	Signal: 87SV supervision pickup.
	Pickup A	295	1	3	Bit	0x20 (6)	-	Signal: 87SV supervision pickup (Phase A).
	Pickup B	295	1	3	Bit	0x40 (7)	-	Signal: 87SV supervision pickup (Phase B).
	Pickup C	295	1	3	Bit	0x80 (8)	-	Signal: 87SV supervision pickup (Phase C).
	Alarm (*)	295	1	3	Bit	0x100 (9)	-	Signal: 87SV supervision alarm.
	Alarm A (*)	295	1	3	Bit	0x200 (10)	-	Signal: 87SV supervision alarm (Phase A).
	Alarm B (*)	295	1	3	Bit	0x400 (11)	-	Signal: 87SV supervision alarm (Phase B).
	Alarm C (*)	295	1	3	Bit	0x800	-	Signal: 87SV supervision alarm (Phase C).

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(12)		
87SV[2] - Open CT Supervision		296	1	3	Struct			
	ExBlo1-I	296	1	3	Bit	0x1 (1)	-	Module Input State: External Blocking1
	ExBlo2-I	296	1	3	Bit	0x2 (2)	-	Module Input State: External Blocking2
	Active	296	1	3	Bit	0x4 (3)	-	Signal: Active
	ExBlo	296	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Pickup	296	1	3	Bit	0x10 (5)	-	Signal: 87SV supervision pickup.
	Pickup A	296	1	3	Bit	0x20 (6)	-	Signal: 87SV supervision pickup (Phase A).
	Pickup B	296	1	3	Bit	0x40 (7)	-	Signal: 87SV supervision pickup (Phase B).
	Pickup C	296	1	3	Bit	0x80 (8)	-	Signal: 87SV supervision pickup (Phase C).
	Alarm (*)	296	1	3	Bit	0x100 (9)	-	Signal: 87SV supervision alarm.
	Alarm A (*)	296	1	3	Bit	0x200	-	Signal: 87SV supervision alarm (Phase A).

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(10)		
	Alarm B (*)	296	1	3	Bit	0x400 (11)	-	Signal: 87SV supervision alarm (Phase B).
	Alarm C (*)	296	1	3	Bit	0x800 (12)	-	Signal: 87SV supervision alarm (Phase C).
87SV[3] - Open CT Supervision		297	1	3	Struct			
	ExBlo1-I	297	1	3	Bit	0x1 (1)	-	Module Input State: External Blocking1
	ExBlo2-I	297	1	3	Bit	0x2 (2)	-	Module Input State: External Blocking2
	Active	297	1	3	Bit	0x4 (3)	-	Signal: Active
	ExBlo	297	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Pickup	297	1	3	Bit	0x10 (5)	-	Signal: 87SV supervision pickup.
	Pickup A	297	1	3	Bit	0x20 (6)	-	Signal: 87SV supervision pickup (Phase A).
	Pickup B	297	1	3	Bit	0x40 (7)	-	Signal: 87SV supervision pickup (Phase B).
	Pickup C	297	1	3	Bit	0x80	-	Signal: 87SV supervision pickup (Phase C).

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(8)		
	Alarm (*)	297	1	3	Bit	0x100 (9)	-	Signal: 87SV supervision alarm.
	Alarm A (*)	297	1	3	Bit	0x200 (10)	-	Signal: 87SV supervision alarm (Phase A).
	Alarm B (*)	297	1	3	Bit	0x400 (11)	-	Signal: 87SV supervision alarm (Phase B).
	Alarm C (*)	297	1	3	Bit	0x800 (12)	-	Signal: 87SV supervision alarm (Phase C).
87[1] - Overcurrent Differential Protection		289	1	3	Struct			
	ExBlo1-I	289	1	3	Bit	0x1 (1)	-	Module Input State: External Blocking1
	ExBlo2-I	289	1	3	Bit	0x2 (2)	-	Module Input State: External Blocking2
	ExBlo TripCmd-I	289	1	3	Bit	0x4 (3)	-	Module Input State: External Blocking of the Trip Command
	Ex Rel. Check Zone-I	289	1	3	Bit	0x8 (4)	-	Module input state: External release of the zone check feature.
	Active	289	1	3	Bit	0x10 (5)	-	Signal: Active

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	ExBlo	289	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Check Zone released	289	1	3	Bit	0x40 (7)	-	Signal: The zone check feature has been released.
	Pickup	289	1	3	Bit	0x80 (8)	-	Signal: 87 protection has started.
	Pickup A	289	1	3	Bit	0x100 (9)	-	Signal: 87 protection has started (Phase A).
	Pickup B	289	1	3	Bit	0x200 (10)	-	Signal: 87 protection has started (Phase B).
	Pickup C	289	1	3	Bit	0x400 (11)	-	Signal: 87 protection has started (Phase C).
87[1] - Overcurrent Differential Protection		290	1	3	Struct			
	Trip (*)	290	1	3	Bit	0x1 (1)	-	Signal: 87 protection has tripped.
	Trip A (*)	290	1	3	Bit	0x2 (2)	-	Signal: 87 protection has tripped (Phase A).
	Trip B (*)	290	1	3	Bit	0x4 (3)	-	Signal: 87 protection has tripped (Phase B).
	Trip C (*)	290	1	3	Bit	0x8	-	Signal: 87 protection has tripped (Phase C).

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(4)		
	TripCmd (*)	290	1	3	Bit	0x10 (5)	-	Signal: Trip Command
	Blo TripCmd	290	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	290	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
87[2] - Overcurrent Differential Protection		291	1	3	Struct			
	ExBlo1-I	291	1	3	Bit	0x1 (1)	-	Module Input State: External Blocking1
	ExBlo2-I	291	1	3	Bit	0x2 (2)	-	Module Input State: External Blocking2
	ExBlo TripCmd-I	291	1	3	Bit	0x4 (3)	-	Module Input State: External Blocking of the Trip Command
	Ex Rel. Check Zone-I	291	1	3	Bit	0x8 (4)	-	Module input state: External release of the zone check feature.
	Active	291	1	3	Bit	0x10 (5)	-	Signal: Active
	ExBlo	291	1	3	Bit	0x20 (6)	-	Signal: External Blocking

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Check Zone released	291	1	3	Bit	0x40 (7)	-	Signal: The zone check feature has been released.
	Pickup	291	1	3	Bit	0x80 (8)	-	Signal: 87 protection has started.
	Pickup A	291	1	3	Bit	0x100 (9)	-	Signal: 87 protection has started (Phase A).
	Pickup B	291	1	3	Bit	0x200 (10)	-	Signal: 87 protection has started (Phase B).
	Pickup C	291	1	3	Bit	0x400 (11)	-	Signal: 87 protection has started (Phase C).
87[2] - Overcurrent Differential Protection		292	1	3	Struct			
	Trip (*)	292	1	3	Bit	0x1 (1)	-	Signal: 87 protection has tripped.
	Trip A (*)	292	1	3	Bit	0x2 (2)	-	Signal: 87 protection has tripped (Phase A).
	Trip B (*)	292	1	3	Bit	0x4 (3)	-	Signal: 87 protection has tripped (Phase B).
	Trip C (*)	292	1	3	Bit	0x8 (4)	-	Signal: 87 protection has tripped (Phase C).
	TripCmd (*)	292	1	3	Bit	0x10	-	Signal: Trip Command

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(5)		
	Blo TripCmd	292	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	292	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
87[3] - Overcurrent Differential Protection		293	1	3	Struct			
	ExBlo1-I	293	1	3	Bit	0x1 (1)	-	Module Input State: External Blocking1
	ExBlo2-I	293	1	3	Bit	0x2 (2)	-	Module Input State: External Blocking2
	ExBlo TripCmd-I	293	1	3	Bit	0x4 (3)	-	Module Input State: External Blocking of the Trip Command
	Ex Rel. Check Zone-I	293	1	3	Bit	0x8 (4)	-	Module input state: External release of the zone check feature.
	Active	293	1	3	Bit	0x10 (5)	-	Signal: Active
	ExBlo	293	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Check Zone released	293	1	3	Bit	0x40 (7)	-	Signal: The zone check feature has been released.

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Pickup	293	1	3	Bit	0x80 (8)	-	Signal: 87 protection has started.
	Pickup A	293	1	3	Bit	0x100 (9)	-	Signal: 87 protection has started (Phase A).
	Pickup B	293	1	3	Bit	0x200 (10)	-	Signal: 87 protection has started (Phase B).
	Pickup C	293	1	3	Bit	0x400 (11)	-	Signal: 87 protection has started (Phase C).
87[3] - Overcurrent Differential Protection		294	1	3	Struct			
	Trip (*)	294	1	3	Bit	0x1 (1)	-	Signal: 87 protection has tripped.
	Trip A (*)	294	1	3	Bit	0x2 (2)	-	Signal: 87 protection has tripped (Phase A).
	Trip B (*)	294	1	3	Bit	0x4 (3)	-	Signal: 87 protection has tripped (Phase B).
	Trip C (*)	294	1	3	Bit	0x8 (4)	-	Signal: 87 protection has tripped (Phase C).
	TripCmd (*)	294	1	3	Bit	0x10 (5)	-	Signal: Trip Command
	Blo TripCmd	294	1	3	Bit	0x20	-	Signal: Trip Command blocked

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(6)		
	ExBlo TripCmd	294	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
Ctrl		176	1	3	Struct			
	Local	176	1	3	Bit	0x1 (1)	-	Switching Authority: Local
	Remote	176	1	3	Bit	0x2 (2)	-	Switching Authority: Remote
	SG Disturb	176	1	3	Bit	0x8 (4)	-	(At least one) Switch is disturbed.
	SG Indeterm	176	1	3	Bit	0x10 (5)	-	(At least one) Switch is moving (Position cannot be determined).
DI-8P X1		1008	1	3	Struct			
	DI 1	1008	1	3	Bit	0x1 (1)	-	Signal: Digital Input
	DI 2	1008	1	3	Bit	0x2 (2)	-	Signal: Digital Input
	DI 3	1008	1	3	Bit	0x4 (3)	-	Signal: Digital Input
	DI 4	1008	1	3	Bit	0x8 (4)	-	Signal: Digital Input
	DI 5	1008	1	3	Bit	0x10	-	Signal: Digital Input

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(5)		
	DI 6	1008	1	3	Bit	0x20 (6)	-	Signal: Digital Input
	DI 7	1008	1	3	Bit	0x40 (7)	-	Signal: Digital Input
	DI 8	1008	1	3	Bit	0x80 (8)	-	Signal: Digital Input
ExP[1] – Ext Protection		49	1	3	Struct			
	ExBlo1-I	49	1	3	Bit	0x1 (1)	-	Module Input State: External Blocking1
	ExBlo2-I	49	1	3	Bit	0x2 (2)	-	Module Input State: External Blocking2
	ExBlo TripCmd-I	49	1	3	Bit	0x4 (3)	-	Module Input State: External Blocking of the Trip Command
	Alarm-I	49	1	3	Bit	0x8 (4)	-	Module Input State: Alarm
	Trip-I	49	1	3	Bit	0x10 (5)	-	Module Input State: Trip
	Active	49	1	3	Bit	0x20 (6)	-	Signal: Active
	ExBlo	49	1	3	Bit	0x40	-	Signal: External Blocking

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(7)		
	Blo TripCmd	49	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	49	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	Alarm	49	1	3	Bit	0x200 (10)	-	Signal: Alarm
	Trip (*)	49	1	3	Bit	0x400 (11)	-	Signal: Trip
	TripCmd (*)	49	1	3	Bit	0x800 (12)	-	Signal: Trip Command
Exp[2] - Ext Protection		50	1	3	Struct			
	ExBlo1-I	50	1	3	Bit	0x1 (1)	-	Module Input State: External Blocking1
	ExBlo2-I	50	1	3	Bit	0x2 (2)	-	Module Input State: External Blocking2
	ExBlo TripCmd-I	50	1	3	Bit	0x4 (3)	-	Module Input State: External Blocking of the Trip Command
	Alarm-I	50	1	3	Bit	0x8 (4)	-	Module Input State: Alarm
	Trip-I	50	1	3	Bit	0x10	-	Module Input State: Trip

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(5)		
	Active	50	1	3	Bit	0x20 (6)	-	Signal: Active
	ExBlo	50	1	3	Bit	0x40 (7)	-	Signal: External Blocking
	Blo TripCmd	50	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	50	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	Alarm	50	1	3	Bit	0x200 (10)	-	Signal: Alarm
	Trip (*)	50	1	3	Bit	0x400 (11)	-	Signal: Trip
	TripCmd (*)	50	1	3	Bit	0x800 (12)	-	Signal: Trip Command
ExP[3] - Ext Protection		51	1	3	Struct			
	ExBlo1-I	51	1	3	Bit	0x1 (1)	-	Module Input State: External Blocking1
	ExBlo2-I	51	1	3	Bit	0x2 (2)	-	Module Input State: External Blocking2
	ExBlo TripCmd-I	51	1	3	Bit	0x4	-	Module Input State: External Blocking of the Trip Command

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(3)		
	Alarm-I	51	1	3	Bit	0x8 (4)	-	Module Input State: Alarm
	Trip-I	51	1	3	Bit	0x10 (5)	-	Module Input State: Trip
	Active	51	1	3	Bit	0x20 (6)	-	Signal: Active
	ExBlo	51	1	3	Bit	0x40 (7)	-	Signal: External Blocking
	Blo TripCmd	51	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	51	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	Alarm	51	1	3	Bit	0x200 (10)	-	Signal: Alarm
	Trip (*)	51	1	3	Bit	0x400 (11)	-	Signal: Trip
	TripCmd (*)	51	1	3	Bit	0x800 (12)	-	Signal: Trip Command
ExP[4] - Ext Protection		52	1	3	Struct			
	ExBlo1-I	52	1	3	Bit	0x1	-	Module Input State: External Blocking1

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(1)		
	ExBlo2-I	52	1	3	Bit	0x2 (2)	-	Module Input State: External Blocking2
	ExBlo TripCmd-I	52	1	3	Bit	0x4 (3)	-	Module Input State: External Blocking of the Trip Command
	Alarm-I	52	1	3	Bit	0x8 (4)	-	Module Input State: Alarm
	Trip-I	52	1	3	Bit	0x10 (5)	-	Module Input State: Trip
	Active	52	1	3	Bit	0x20 (6)	-	Signal: Active
	ExBlo	52	1	3	Bit	0x40 (7)	-	Signal: External Blocking
	Blo TripCmd	52	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	52	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	Alarm	52	1	3	Bit	0x200 (10)	-	Signal: Alarm
	Trip (*)	52	1	3	Bit	0x400 (11)	-	Signal: Trip
	TripCmd (*)	52	1	3	Bit	0x800	-	Signal: Trip Command

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(12)		
Fast Status Register		5000	1	3	Struct			
	Device Type	5000	1	3	Bit	0xffff (1)	-	Device type code for relationship between device name and its Modbus code. For Eaton E-Series: EDR-3000 - 2 EDR-5000 - 3 EMR-3000 - 4 EMR-3MP0 - 4 EMR-4000 - 5 EMR-5000 - 6 ETR-4000 - 8 ETR-5000 - 9 EGR-5000 - 12 EBR-3000 - 13
Fast Status Register		5001	1	3	Struct			
	Comm Version	5001	1	3	Bit	0xffff (1)	-	Modbus Communication version. This version number changes if something becomes incompatible between different Modbus releases.
Fast Status Register		5002	1	3	Struct			
	Config Bin Inp1-l	5002	1	3	Bit	0x1 (1)	-	State of the module input: Config Bin Inp

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Config Bin Inp2-l	5002	1	3	Bit	0x2 (2)	-	State of the module input: Config Bin Inp
	Config Bin Inp3-l	5002	1	3	Bit	0x4 (3)	-	State of the module input: Config Bin Inp
	Config Bin Inp4-l	5002	1	3	Bit	0x8 (4)	-	State of the module input: Config Bin Inp
	Config Bin Inp5-l	5002	1	3	Bit	0x10 (5)	-	State of the module input: Config Bin Inp
	Config Bin Inp6-l	5002	1	3	Bit	0x20 (6)	-	State of the module input: Config Bin Inp
	Config Bin Inp7-l	5002	1	3	Bit	0x40 (7)	-	State of the module input: Config Bin Inp
	Config Bin Inp8-l	5002	1	3	Bit	0x80 (8)	-	State of the module input: Config Bin Inp
	Config Bin Inp9-l	5002	1	3	Bit	0x100 (9)	-	State of the module input: Config Bin Inp
	Config Bin Inp10-l	5002	1	3	Bit	0x200 (10)	-	State of the module input: Config Bin Inp
	Config Bin Inp11-l	5002	1	3	Bit	0x400 (11)	-	State of the module input: Config Bin Inp
	Config Bin Inp12-l	5002	1	3	Bit	0x800	-	State of the module input: Config Bin Inp

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(12)		
	Config Bin Inp13-I	5002	1	3	Bit	0x1000 (13)	-	State of the module input: Config Bin Inp
	Config Bin Inp14-I	5002	1	3	Bit	0x2000 (14)	-	State of the module input: Config Bin Inp
	Config Bin Inp15-I	5002	1	3	Bit	0x4000 (15)	-	State of the module input: Config Bin Inp
	Config Bin Inp16-I	5002	1	3	Bit	0x8000 (16)	-	State of the module input: Config Bin Inp
Fast Status Register		5003	1	3	Struct			
	Config Bin Inp17-I	5003	1	3	Bit	0x1 (1)	-	State of the module input: Config Bin Inp
	Config Bin Inp18-I	5003	1	3	Bit	0x2 (2)	-	State of the module input: Config Bin Inp
	Config Bin Inp19-I	5003	1	3	Bit	0x4 (3)	-	State of the module input: Config Bin Inp
	Config Bin Inp20-I	5003	1	3	Bit	0x8 (4)	-	State of the module input: Config Bin Inp
	Config Bin Inp21-I	5003	1	3	Bit	0x10 (5)	-	State of the module input: Config Bin Inp
	Config Bin Inp22-I	5003	1	3	Bit	0x20	-	State of the module input: Config Bin Inp

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(6)		
	Config Bin Inp23-I	5003	1	3	Bit	0x40 (7)	-	State of the module input: Config Bin Inp
	Config Bin Inp24-I	5003	1	3	Bit	0x80 (8)	-	State of the module input: Config Bin Inp
	Config Bin Inp25-I	5003	1	3	Bit	0x100 (9)	-	State of the module input: Config Bin Inp
	Config Bin Inp26-I	5003	1	3	Bit	0x200 (10)	-	State of the module input: Config Bin Inp
	Config Bin Inp27-I	5003	1	3	Bit	0x400 (11)	-	State of the module input: Config Bin Inp
	Config Bin Inp28-I	5003	1	3	Bit	0x800 (12)	-	State of the module input: Config Bin Inp
	Config Bin Inp29-I	5003	1	3	Bit	0x1000 (13)	-	State of the module input: Config Bin Inp
	Config Bin Inp30-I	5003	1	3	Bit	0x2000 (14)	-	State of the module input: Config Bin Inp
	Config Bin Inp31-I	5003	1	3	Bit	0x4000 (15)	-	State of the module input: Config Bin Inp
	Config Bin Inp32-I	5003	1	3	Bit	0x8000 (16)	-	State of the module input: Config Bin Inp

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
Fast Status Register		5004	1	3	Struct			
	Trip Cause (*)	5004	1	3	Bit	0xffff (1)	-	Initial reason of trip. It is presented as an integer value and corresponds to the "Trip" entry in the fault record, which refers to the name of the protective module that tripped first. Look up the definition of these integer values (i. e. the mapping trip code number->module name) in the "Cause of Trip" table within the SCADA documentation.
IRIG-B		148	1	3	Struct			
	IRIG-B active	148	1	3	Bit	0x1 (1)	-	Signal: If there is no valid IRIG-B signal for 60 sec, IRIG-B is regarded as inactive.
	High-Low Invert	148	1	3	Bit	0x2 (2)	-	Signal: The High and Low signals of the IRIG-B are inverted. This does NOT mean that the wiring is faulty. If the wiring is faulty no IRIG-B signal will be detected.
Logic		1100	1	3	Struct			
	LE1.Gate Out	1100	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE1.Timer Out	1100	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE1.Out	1100	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE1.Out inverted	1100	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE1.Gate In1-I	1100	1	3	Bit	0x10	-	State of the module input: Assignment of the Input Signal

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(5)		
	LE1.Gate In2-I	1100	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE1.Gate In3-I	1100	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE1.Gate In4-I	1100	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE1.Reset Latch-I	1100	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logic		1101	1	3	Struct			
	LE2.Gate Out	1101	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE2.Timer Out	1101	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE2.Out	1101	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE2.Out inverted	1101	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE2.Gate In1-I	1101	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE2.Gate In2-I	1101	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE2.Gate In3-I	1101	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE2.Gate In4-I	1101	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE2.Reset Latch-I	1101	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logic		1102	1	3	Struct			
	LE3.Gate Out	1102	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE3.Timer Out	1102	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE3.Out	1102	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE3.Out inverted	1102	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE3.Gate In1-I	1102	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE3.Gate In2-I	1102	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE3.Gate In3-I	1102	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE3.Gate In4-I	1102	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE3.Reset Latch-I	1102	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logic		1103	1	3	Struct			
	LE4.Gate Out	1103	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE4.Timer Out	1103	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE4.Out	1103	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE4.Out inverted	1103	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE4.Gate In1-I	1103	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE4.Gate In2-I	1103	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE4.Gate In3-I	1103	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE4.Gate In4-I	1103	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE4.Reset Latch-I	1103	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logic		1104	1	3	Struct			
	LE5.Gate Out	1104	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE5.Timer Out	1104	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE5.Out	1104	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE5.Out inverted	1104	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE5.Gate In1-I	1104	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE5.Gate In2-I	1104	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE5.Gate In3-I	1104	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE5.Gate In4-I	1104	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE5.Reset Latch-I	1104	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logic		1105	1	3	Struct			

Module (ANSI / IEC)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE6.Gate Out	1105	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE6.Timer Out	1105	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE6.Out	1105	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE6.Out inverted	1105	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE6.Gate In1-I	1105	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE6.Gate In2-I	1105	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE6.Gate In3-I	1105	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE6.Gate In4-I	1105	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE6.Reset Latch-I	1105	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logic		1106	1	3	Struct			
	LE7.Gate Out	1106	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE7.Timer Out	1106	1	3	Bit	0x2	-	Signal: Timer Output

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(2)		
	LE7.Out	1106	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE7.Out inverted	1106	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE7.Gate In1-I	1106	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE7.Gate In2-I	1106	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE7.Gate In3-I	1106	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE7.Gate In4-I	1106	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE7.Reset Latch-I	1106	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logic		1107	1	3	Struct			
	LE8.Gate Out	1107	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE8.Timer Out	1107	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE8.Out	1107	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE8.Out inverted	1107	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE8.Gate In1-I	1107	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE8.Gate In2-I	1107	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE8.Gate In3-I	1107	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE8.Gate In4-I	1107	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE8.Reset Latch-I	1107	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logic		1108	1	3	Struct			
	LE9.Gate Out	1108	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE9.Timer Out	1108	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE9.Out	1108	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE9.Out inverted	1108	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE9.Gate In1-I	1108	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE9.Gate In2-I	1108	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE9.Gate In3-I	1108	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE9.Gate In4-I	1108	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE9.Reset Latch-I	1108	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logic		1109	1	3	Struct			
	LE10.Gate Out	1109	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE10.Timer Out	1109	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE10.Out	1109	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE10.Out inverted	1109	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE10.Gate In1-I	1109	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE10.Gate In2-I	1109	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE10.Gate In3-I	1109	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE10.Gate In4-I	1109	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE10.Reset Latch-I	1109	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logic		1110	1	3	Struct			
	LE11.Gate Out	1110	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE11.Timer Out	1110	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE11.Out	1110	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE11.Out inverted	1110	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE11.Gate In1-I	1110	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE11.Gate In2-I	1110	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE11.Gate In3-I	1110	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE11.Gate In4-I	1110	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE11.Reset Latch-I	1110	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logic		1111	1	3	Struct			
	LE12.Gate Out	1111	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE12.Timer Out	1111	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE12.Out	1111	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE12.Out inverted	1111	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE12.Gate In1-I	1111	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE12.Gate In2-I	1111	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE12.Gate In3-I	1111	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE12.Gate In4-I	1111	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE12.Reset Latch-I	1111	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logic		1112	1	3	Struct			
	LE13.Gate Out	1112	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE13.Timer Out	1112	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE13.Out	1112	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE13.Out inverted	1112	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE13.Gate In1-I	1112	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE13.Gate In2-I	1112	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE13.Gate In3-I	1112	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE13.Gate In4-I	1112	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE13.Reset Latch-I	1112	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logic		1113	1	3	Struct			
	LE14.Gate Out	1113	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE14.Timer Out	1113	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE14.Out	1113	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE14.Out inverted	1113	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE14.Gate In1-I	1113	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE14.Gate In2-I	1113	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE14.Gate In3-I	1113	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE14.Gate In4-I	1113	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE14.Reset Latch-I	1113	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logic		1114	1	3	Struct			

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE15.Gate Out	1114	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE15.Timer Out	1114	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE15.Out	1114	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE15.Out inverted	1114	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE15.Gate In1-I	1114	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE15.Gate In2-I	1114	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE15.Gate In3-I	1114	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE15.Gate In4-I	1114	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE15.Reset Latch-I	1114	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logic		1115	1	3	Struct			
	LE16.Gate Out	1115	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE16.Timer Out	1115	1	3	Bit	0x2	-	Signal: Timer Output

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(2)		
	LE16.Out	1115	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE16.Out inverted	1115	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE16.Gate In1-I	1115	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE16.Gate In2-I	1115	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE16.Gate In3-I	1115	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE16.Gate In4-I	1115	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE16.Reset Latch-I	1115	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logic		1116	1	3	Struct			
	LE17.Gate Out	1116	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE17.Timer Out	1116	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE17.Out	1116	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE17.Out inverted	1116	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE17.Gate In1-I	1116	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE17.Gate In2-I	1116	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE17.Gate In3-I	1116	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE17.Gate In4-I	1116	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE17.Reset Latch-I	1116	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logic		1117	1	3	Struct			
	LE18.Gate Out	1117	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE18.Timer Out	1117	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE18.Out	1117	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE18.Out inverted	1117	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE18.Gate In1-I	1117	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE18.Gate In2-I	1117	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE18.Gate In3-I	1117	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE18.Gate In4-I	1117	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE18.Reset Latch-I	1117	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logic		1118	1	3	Struct			
	LE19.Gate Out	1118	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE19.Timer Out	1118	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE19.Out	1118	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE19.Out inverted	1118	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE19.Gate In1-I	1118	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE19.Gate In2-I	1118	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE19.Gate In3-I	1118	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE19.Gate In4-I	1118	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE19.Reset Latch-I	1118	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logic		1119	1	3	Struct			
	LE20.Gate Out	1119	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE20.Timer Out	1119	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE20.Out	1119	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE20.Out inverted	1119	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE20.Gate In1-I	1119	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE20.Gate In2-I	1119	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE20.Gate In3-I	1119	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE20.Gate In4-I	1119	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE20.Reset Latch-I	1119	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Modbus		1005	1	3	Struct			
	Comm Cmd 1	1005	1	3	Bit	0x1 (1)	-	Communication Command
	Comm Cmd 2	1005	1	3	Bit	0x2 (2)	-	Communication Command
	Comm Cmd 3	1005	1	3	Bit	0x4 (3)	-	Communication Command
	Comm Cmd 4	1005	1	3	Bit	0x8 (4)	-	Communication Command
	Comm Cmd 5	1005	1	3	Bit	0x10 (5)	-	Communication Command
	Comm Cmd 6	1005	1	3	Bit	0x20 (6)	-	Communication Command
	Comm Cmd 7	1005	1	3	Bit	0x40 (7)	-	Communication Command
	Comm Cmd 8	1005	1	3	Bit	0x80	-	Communication Command

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(8)		
	Comm Cmd 9	1005	1	3	Bit	0x100 (9)	-	Communication Command
	Comm Cmd 10	1005	1	3	Bit	0x200 (10)	-	Communication Command
	Comm Cmd 11	1005	1	3	Bit	0x400 (11)	-	Communication Command
	Comm Cmd 12	1005	1	3	Bit	0x800 (12)	-	Communication Command
	Comm Cmd 13	1005	1	3	Bit	0x1000 (13)	-	Communication Command
	Comm Cmd 14	1005	1	3	Bit	0x2000 (14)	-	Communication Command
	Comm Cmd 15	1005	1	3	Bit	0x4000 (15)	-	Communication Command
	Comm Cmd 16	1005	1	3	Bit	0x8000 (16)	-	Communication Command
PSet-Switch		59	1	3	Struct			
	PS 1	59	1	3	Bit	0x1 (1)	-	Signal: The currently active Parameter Set is PS 1
	PS 2	59	1	3	Bit	0x2 (2)	-	Signal: The currently active Parameter Set is PS 2

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	PS 3	59	1	3	Bit	0x4 (3)	-	Signal: The currently active Parameter Set is PS 3
	PS 4	59	1	3	Bit	0x8 (4)	-	Signal: The currently active Parameter Set is PS 4
	PSS manual	59	1	3	Bit	0x10 (5)	-	Signal: Manual switch over of a Parameter Set
	PSS via Comm	59	1	3	Bit	0x20 (6)	-	Signal: Parameter Set Switch via Scada. Write into this output byte the integer of the parameter set that should become active (e.g. 4 => Switch onto parameter set 4).
	PSS via Inp fct	59	1	3	Bit	0x40 (7)	-	Signal: Parameter Set Switch via Input Function
	PS1-I	59	1	3	Bit	0x80 (8)	-	State of the module input, respectively of the signal, that should activate this Parameter Setting Group.
	PS2-I	59	1	3	Bit	0x100 (9)	-	State of the module input, respectively of the signal, that should activate this Parameter Setting Group.
	PS3-I	59	1	3	Bit	0x200 (10)	-	State of the module input, respectively of the signal, that should activate this Parameter Setting Group.
	PS4-I	59	1	3	Bit	0x400 (11)	-	State of the module input, respectively of the signal, that should activate this Parameter Setting Group.
	Min. 1 param changed (*)	59	1	3	Bit	0x800 (12)	-	Signal: At least one parameter has been changed
Prot		1	1	3	Struct			

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	ExBlo1-I	1	1	3	Bit	0x1 (1)	-	Module Input State: External Blocking1
	ExBlo2-I	1	1	3	Bit	0x2 (2)	-	Module Input State: External Blocking2
	Active	1	1	3	Bit	0x4 (3)	-	Signal: Active
	ExBlo	1	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Pickup Phase A	1	1	3	Bit	0x10 (5)	-	Signal: General Pickup Phase A
	Pickup Phase B	1	1	3	Bit	0x20 (6)	-	Signal: General Pickup Phase B
	Pickup Phase C	1	1	3	Bit	0x40 (7)	-	Signal: General Pickup Phase C
	Pickup	1	1	3	Bit	0x100 (9)	-	Signal: General Pickup
	Trip Phase A (*)	1	1	3	Bit	0x200 (10)	-	Signal: General Trip Phase A
	Trip Phase B (*)	1	1	3	Bit	0x400 (11)	-	Signal: General Trip Phase B
	Trip Phase C (*)	1	1	3	Bit	0x800	-	Signal: General Trip Phase C

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(12)		
	Trip (*)	1	1	3	Bit	0x2000 (14)	-	Signal: General Trip
Prot		57	1	3	Struct			
	Fault No.	57	1	3	Bit	0xffff (1)	-	Waveform number
RO-5 X2 - MK IRIG (unten)		1004	1	3	Struct			
	RO 1	1004	1	3	Bit	0x1 (1)	-	Signal: Relay Output
	RO 2	1004	1	3	Bit	0x2 (2)	-	Signal: Relay Output
	RO 3	1004	1	3	Bit	0x4 (3)	-	Signal: Relay Output
	RO 4	1004	1	3	Bit	0x8 (4)	-	Signal: Relay Output
	RO 5	1004	1	3	Bit	0x10 (5)	-	Signal: Relay Output
	DISARMED!	1004	1	3	Bit	0x40 (7)	-	Signal: CAUTION! RELAYS DISARMED in order to safely perform maintenance while eliminating the risk of taking an entire process off-line. (Note: Zone Interlocking and Supervision Contact cannot be disarmed). YOU MUST ENSURE that the relays are ARMED AGAIN after maintenance

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Outs forced	1004	1	3	Bit	0x80 (8)	-	Signal: The State of at least one Relay Output has been set by force. That means that the state of at least one Relay is forced and hence does not show the state of the assigned signals.
SSV		273	1	3	Struct			
	System Error	273	1	3	Bit	0x1 (1)	-	Signal: Device Failure
	New error (*)	273	1	3	Bit	0x8 (4)	-	Signal: A new error message has been issued.
	New warning (*)	273	1	3	Bit	0x10 (5)	-	Signal: A new warning message has been issued.
	Active	273	1	3	Bit	0x20 (6)	-	Signal: Active
Sgen		1012	1	3	Struct			
	ExBlo1-I	1012	1	3	Bit	0x1 (1)	-	Module Input State: External Blocking1
	Ex ForcePost-I	1012	1	3	Bit	0x2 (2)	-	State of the module input:Force Post state. Abort simulation.
	Running	1012	1	3	Bit	0x10 (5)	-	Signal: Measuring value simulation is running
	State	1012	1	3	Bit	0xe0 (6)	-	Signal: Wave generation states: 0=Off, 1=PreFault, 2=Fault, 3=PostFault, 4=InitReset

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Ex Start Simulation-I	1012	1	3	Bit	0x100 (9)	-	State of the module input: External Start of Fault Simulation (Using the test parameters)
	ExBlo2-I	1012	1	3	Bit	0x200 (10)	-	Module Input State: External Blocking2
	Manual Start	1012	1	3	Bit	0x400 (11)	-	Fault Simulation has been started manually.
	Manual Stop	1012	1	3	Bit	0x800 (12)	-	Fault Simulation has been stopped manually.
	Started	1012	1	3	Bit	0x1000 (13)	-	Fault Simulation has been started
	Stopped	1012	1	3	Bit	0x2000 (14)	-	Fault Simulation has been stopped
Sys		154	1	3	Struct			
	Program Mode-I	154	1	3	Bit	0x40 (7)	-	State of the module input: Parameters can only be changed as long as this input is true. (This means the parameter settings are locked when this input is false.)
	SNTP active	154	1	3	Bit	0x80 (8)	-	Signal: If there is no valid SNTP signal for 120 sec, SNTP is regarded as inactive.
	Program Mode Bypass	154	1	3	Bit	0x100 (9)	-	Signal: Short-period bypass of the Program Mode.
TCM[1] - Trip Coil Monitor		150	1	3	Struct			
	ExBlo1-I	150	1	3	Bit	0x1	-	Module Input State: External Blocking1

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(1)		
	ExBlo2-I	150	1	3	Bit	0x2 (2)	-	Module Input State: External Blocking2
	Active	150	1	3	Bit	0x4 (3)	-	Signal: Active
	ExBlo	150	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Pickup	150	1	3	Bit	0x10 (5)	-	Signal: Pickup Trip Circuit Supervision
	Not Possible	150	1	3	Bit	0x20 (6)	-	Not possible because no state indicator has been assigned to the Lockout Relay.
	Cin NC-I	150	1	3	Bit	0x100 (9)	-	Module input state: This is the feedback signal indicating that the Lockout Relay is in the RESET position. (This is normally implemented by using the NC contact.)
	Cin NO-I	150	1	3	Bit	0x200 (10)	-	Module input state: This is the feedback signal indicating that the Lockout Relay is in the TRIPPED position. (This is normally implemented by using the NO contact.)
TCM[2] - Trip Coil Monitor		151	1	3	Struct			
	ExBlo1-I	151	1	3	Bit	0x1 (1)	-	Module Input State: External Blocking1
	ExBlo2-I	151	1	3	Bit	0x2 (2)	-	Module Input State: External Blocking2

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Active	151	1	3	Bit	0x4 (3)	-	Signal: Active
	ExBlo	151	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Pickup	151	1	3	Bit	0x10 (5)	-	Signal: Pickup Trip Circuit Supervision
	Not Possible	151	1	3	Bit	0x20 (6)	-	Not possible because no state indicator has been assigned to the Lockout Relay.
	Cin NC-I	151	1	3	Bit	0x100 (9)	-	Module input state: This is the feedback signal indicating that the Lockout Relay is in the RESET position. (This is normally implemented by using the NC contact.)
	Cin NO-I	151	1	3	Bit	0x200 (10)	-	Module input state: This is the feedback signal indicating that the Lockout Relay is in the TRIPPED position. (This is normally implemented by using the NO contact.)
TimeSync		54	1	3	Struct			
	synchronized	54	1	3	Bit	0x1 (1)	-	Clock is synchronized.
Wired Inputs		172	1	3	Struct			
	Check Zone-I	172	1	3	Bit	0x2 (2)	-	State of the module input: Input of Check Zone relay.
	86 CT NC-I	172	1	3	Bit	0x4 (3)	-	State of the module input: Position indication of the lockout relay (86 CT / normally closed contact). The signal is inactive (i.e. contact is open) if the lockout relay is in reset state.

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	86 CT NO-I	172	1	3	Bit	0x8 (4)	-	State of the module input: Position indication of the lockout relay (86 CT / normally open contact). The signal is inactive (i.e. contact is open) if the lockout relay is in reset state.
	86T NC-I	172	1	3	Bit	0x10 (5)	-	State of the module input: Position indication of the lockout relay (86 TRIP / normally closed contact). The signal is active (i.e. contact is closed) if the lockout relay is in reset state.
	86T NO-I	172	1	3	Bit	0x20 (6)	-	State of the module input: Position indication of the lockout relay (86 TRIP / normally open contact). The signal is inactive (i.e. contact is open) if the lockout relay is in reset state.

3.2 Measuring Values

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
86[1] - Lockout Relais 86 TRIP	TripCmd Cr	20810	2	4	Float IEE754		-	Counter: Total number of trips of the switch.
86[2] - Lockout Relais 86CT	TripCmd Cr	20822	2	4	Float IEE754		-	Counter: Total number of trips of the switch.
Date and Time		20000	6	4	Struct			
	y	20000	6	4	Short	Word 0 (1)	-	Year
	m	20000	6	4	Short	Word 1 (17)	-	Month
	d	20000	6	4	Short	Word 2 (33)	-	Days
	h	20000	6	4	Short	Word 3 (49)	-	Hours
	min	20000	6	4	Short	Word 4 (65)	-	Minute
	ms	20000	6	4	Short	Word 5 (81)	-	Milliseconds
EBR-Z	87VA	20914	2	4	Float IEE754		V	Measured value: Voltage at the Resistor of the High-Impedance Module, Phase A
EBR-Z	87VB	20916	2	4	Float IEE754		V	Measured value: Voltage at the Resistor of the High-Impedance Module, Phase B
EBR-Z	87VC	20918	2	4	Float IEE754		V	Measured value: Voltage at the Resistor of the High-Impedance Module, Phase C

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
EBR-Z	phi 87VA	20920	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor 87VA Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.
EBR-Z	phi 87VB	20922	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor 87VB Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.
EBR-Z	phi 87VC	20924	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor 87VC Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.
EBR-Z	87VA max	23034	2	4	Float IEE754		V	Measured value: Voltage at the Resistor of the High-Impedance Module, VA maximum value
EBR-Z	87VB max	23040	2	4	Float IEE754		V	Measured value: Voltage at the Resistor of the High-Impedance Module, VB maximum value
EBR-Z	87VC max	23046	2	4	Float IEE754		V	Measured value: Voltage at the Resistor of the High-Impedance Module, VC maximum value
IRIG-B	Edges	20298	2	4	Float IEE754		-	Edges: Total number of rising and falling edges. This signal indicates if a signal is available at the IRIG-B input.
IRIG-B	NoOfFrameErrors	20300	2	4	Float IEE754		-	Total Number of Frame Errors. Physically corrupted Frame.
IRIG-B	NoOfFramesOK	20302	2	4	Float IEE754		-	Total number valid Frames.

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
Modbus	Mapped Meas 1	23000	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 2	23002	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 3	23004	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 4	23006	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 5	23008	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 6	23010	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 7	23012	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 8	23014	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 9	23016	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 10	23018	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 11	23020	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 12	23022	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
Modbus	Mapped Meas 13	23024	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 14	23026	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 15	23028	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 16	23030	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Values	Build	20008	2	4	Float IEE754		-	Build Number
Values	Operating hours Cr	20010	2	4	Float IEE754		h	Operating hours counter of the protective device

3.3 Commands

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
Acknowledge	LEDs	22000	1	5	0xFF00		-	LEDs
Acknowledge	Relay Outputs	22001	1	5	0xFF00		-	Relay Outputs
Acknowledge	SCADA	22002	1	5	0xFF00		-	SCADA Communication
Acknowledge	Device	22003	1	5	0xFF00		-	Device
Acknowledge	Ack TripCmd	22005	1	5	0xFF00		-	Signal: Acknowledge Trip Command
Reset	Modbus diagnosis counter	22006	1	5	0xFF00		-	Modbus diagnosis counter
Comm Cmd	Assbl Comm Cmd 1	22020	1	5	0xFF00=On, 0x0000=Off		-	Assignable Communication Command
Comm Cmd	Assbl Comm Cmd 2	22021	1	5	0xFF00=On, 0x0000=Off		-	Assignable Communication Command
Comm Cmd	Assbl Comm Cmd 3	22022	1	5	0xFF00=On, 0x0000=Off		-	Assignable Communication Command
Comm Cmd	Assbl Comm Cmd 4	22023	1	5	0xFF00=On, 0x0000=Off		-	Assignable Communication Command
Comm Cmd	Assbl Comm Cmd 5	22024	1	5	0xFF00=On, 0x0000=Off		-	Assignable Communication Command
Comm Cmd	Assbl Comm Cmd 6	22025	1	5	0xFF00=On, 0x0000=Off		-	Assignable Communication Command
Comm Cmd	Assbl Comm Cmd 7	22026	1	5	0xFF00=On, 0x0000=Off		-	Assignable Communication Command
Comm Cmd	Assbl Comm Cmd 8	22027	1	5	0xFF00=On, 0x0000=Off		-	Assignable Communication Command
Comm Cmd	Assbl Comm Cmd 9	22028	1	5	0xFF00=On, 0x0000=Off		-	Assignable Communication Command
Comm Cmd	Assbl Comm Cmd 10	22029	1	5	0xFF00=On, 0x0000=Off		-	Assignable Communication Command

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
Comm Cmd	Assbl Comm Cmd 11	22030	1	5	0xFF00=On, 0x0000=Off		-	Assignable Communication Command
Comm Cmd	Assbl Comm Cmd 12	22031	1	5	0xFF00=On, 0x0000=Off		-	Assignable Communication Command
Comm Cmd	Assbl Comm Cmd 13	22032	1	5	0xFF00=On, 0x0000=Off		-	Assignable Communication Command
Comm Cmd	Assbl Comm Cmd 14	22033	1	5	0xFF00=On, 0x0000=Off		-	Assignable Communication Command
Comm Cmd	Assbl Comm Cmd 15	22034	1	5	0xFF00=On, 0x0000=Off		-	Assignable Communication Command
Comm Cmd	Assbl Comm Cmd 16	22035	1	5	0xFF00=On, 0x0000=Off		-	Assignable Communication Command
Fault rec	Res all rec	22040	1	5	0xFF00		-	Reset all records
PSet-Switch	Comm PS1	22050	1	5	0xFF00		-	Communication Setting Group1
PSet-Switch	Comm PS2	22051	1	5	0xFF00		-	Communication Setting Group2
PSet-Switch	Comm PS3	22052	1	5	0xFF00		-	Communication Setting Group3
PSet-Switch	Comm PS4	22053	1	5	0xFF00		-	Communication Setting Group4
Maint Mode	Maint Mode Comm	22054	1	5	0xFF00=On, 0x0000=Off		-	Signal: Arc Flash Reduction Maintenance Comm Mode
Bkr	SG ControlCmd1	22100	1	5	0xFF00=On, 0x0000=Off		-	Control Command Switchgear
Bkr	SG ControlCmd2	22101	1	5	0xFF00=On, 0x0000=Off		-	Control Command Switchgear

3.4 Settings

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
Date and Time		32500	6	3 16	Struct			
	y	32500	6	3 16	Short	Word 0 (1)	-	Year
	m	32500	6	3 16	Short	Word 1 (17)	-	Month
	d	32500	6	3 16	Short	Word 2 (33)	-	Days
	h	32500	6	3 16	Short	Word 3 (49)	-	Hours
	min	32500	6	3 16	Short	Word 4 (65)	-	Minute
	ms	32500	6	3 16	Short	Word 5 (81)	-	Milliseconds
Fault rec		50000	9	3 16	Struct			
	RecordNo	50000	9	3 16	Short	Word 0 (1)	-	Record Number
	Trip Cause	50000	9	3 16	Short	Word 1 (17)	-	Code for the trip cause. In case of several simultaneous trip causes the primary cause is selected. If there is another trip later then the new trip cause overwrites the previous one. The codes for the trip cause are documented in the SCADA documentation.

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Pickup Cause	50000	9	3 16	Short	Word 2 (33)	-	Code for last Pickup cause corresponds to fault record: See scada doc for correlation between pickup reason and code
	Fault No.	50000	9	3 16	Short	Word 3 (49)	-	Waveform number
	No of Grid Faults	50000	9	3 16	Short	Word 4 (65)	-	Number of grid faults: This is a counter for all faults (i.e. General Pickups »Prot . Pickup«), but except faults during a running cycle of the Automatic Reclosure module (signal »AR . Running«). (Remark: The »Fault No.« counts every new fault independent of AR cycles. This means that for protective devices without AR module these two counters are equivalent.)
	Time stamp:	50000	9	3 16	long long	Word 5- Word 8 (81)	-	Timestamp in milliseconds since 1970

3.5 Cause of Trip

Cause of trip reason is provided on two different Modbus addresses:

- At address 5004 the “last primary trip cause” is available. This means, in case of several simultaneous trip causes the primary cause is selected. If there is another trip later then the new trip cause overwrites the previous one. The trip cause can be read as long as a trip reason is present. In addition, the content of this register can be latched. The trip cause is latched in the same way as other trip signals, that means if the corresponding latching setting in Modbus is active, the content of the register is fixed until it is acknowledged by command.
- At address 50000 and up the last trip and alarm reason is available with related record, fault, net number and time stamp. Any saved record can be requested by sending the record number to the corresponding register. Be aware that the content of these registers can only be read entirely and that the content changes every time a new fault occurs in the fault recorder.

All required fault values must be defined within a SCADApter (ErSMap) mapping file. The default datapoint list cannot be used in this case. All data-points that are needed for communicating with the substation must be defined as a ErSMap mapping file. (See also the separate SCADApter User Manual.)

If not a specific fault is selected, last fault value is presented on these addresses.

The following table is showing the “trip cause code” and its relation to the “trip cause reason”.

Trip Cause	Description	Module
1	NORM	
2	EXTERNAL	ExP[1], ExP[2], ExP[3], ExP[4]
160	OC DIFF	87[1], 87[2], 87[3]

3.6 Fast Status Register

At registers ≥ 5000 an area is provided where common status indicator bits can be read from one location. See the Register Maps for information that is available in general.

At registers 5002 and 5003 the user has the option to collect arbitrary device status information. The purpose is to get a custom set of device states in a single request. Some of these positions are already pre-configured.

The following table lists the pre-configured states:

General Function	Bit Position	Short Description	Long Description
Relay Health	0	Health status	
General Trips / General Pickups	1	Pickup	General pickup of any type - not element-specific.
	2	Trip	General Trip of any type - not element-specific.
	3		
	4		
	5		
	6		
	7		
Breaker [*]	8	Pos Open	Breaker is in Open position.
	9	Pos Close	Breaker is in Closed position.
	10	Pickup	Breaker failure detected and timer in progress.
	11	Trip	Breaker failure detected and trip initiated.
	12	Lockout	Breaker failure lockout.
	13		
	14		
	15		

General Function	Bit Position	Short Description	Long Description
Sgen status	16	Running	Test simulation signal generation "Running".
ARMS [**]	17	Maintenance	Maintenance mode active.
Motor [***]	18	Motor Relay Ready - (stopped)	
	19	Motor Relay Start	
	20	Motor Relay Running	

[*] Only available in devices with Breaker Control.

[]** Only available in devices that support Maintenance mode.

[*]** Only available in Motor Relays.

Modbus Datapoints IL02602008E

EBR-3000

Effective 2023-05-17

This instruction leaflet is published solely for information purposes and should not be considered all-inclusive. If further information is required, you should consult an authorized Eaton sales representative.

The sale of the product shown in this literature is subject to the terms and conditions outlined in appropriate Eaton selling policies or other contractual agreement between the parties. This literature is not intended to and does not enlarge or add to any such contract. The sole source governing the rights and remedies of any purchaser of this equipment is the contract between the purchaser and Eaton.

NO WARRANTIES, EXPRESSED OR IMPLIED, INCLUDING WARRANTIES OF FITNESS FOR A PARTICULAR PURPOSE OR MERCHANTABILITY, OR WARRANTIES ARISING FROM COURSE OF DEALING OR USAGE OF TRADE, ARE MADE REGARDING THE INFORMATION, RECOMMENDATIONS, AND DESCRIPTIONS CONTAINED HEREIN.

In no event will Eaton be responsible to the purchaser or user in contract, in tort (including negligence), strict liability or otherwise for any special, indirect, incidental or consequential damage or loss whatsoever, including but not limited to damage or loss of use of equipment, plant or power system, cost of capital, loss of power, additional expenses in the use of existing power facilities, or claims against the purchaser or user by its customers resulting from the use of the information, recommendations and description contained herein.



Powering Business Worldwide

Eaton

Electrical Sector
1000 Eaton Boulevard
Cleveland, OH 44122
United States
Eaton.com

Eaton Technical Support

Phone: 877-ETN-CARE (877-386-2273) option 2 Sub-option 5,1,1
844-435-8982
Email: MRSupport@eaton.com

© 2023 Eaton Corporation

All Rights Reserved

Printed in USA